



 RISC-V Summit Europe 2023

# RISC-V Summit Europe Program Overview

Daniel Müller-Gritschneider – TU Munich – Co-Program Chair

Georg Sigl – TU Munich – Co-Program Chair

Teresa Cervero – BSC – Local Chair

# Program Information

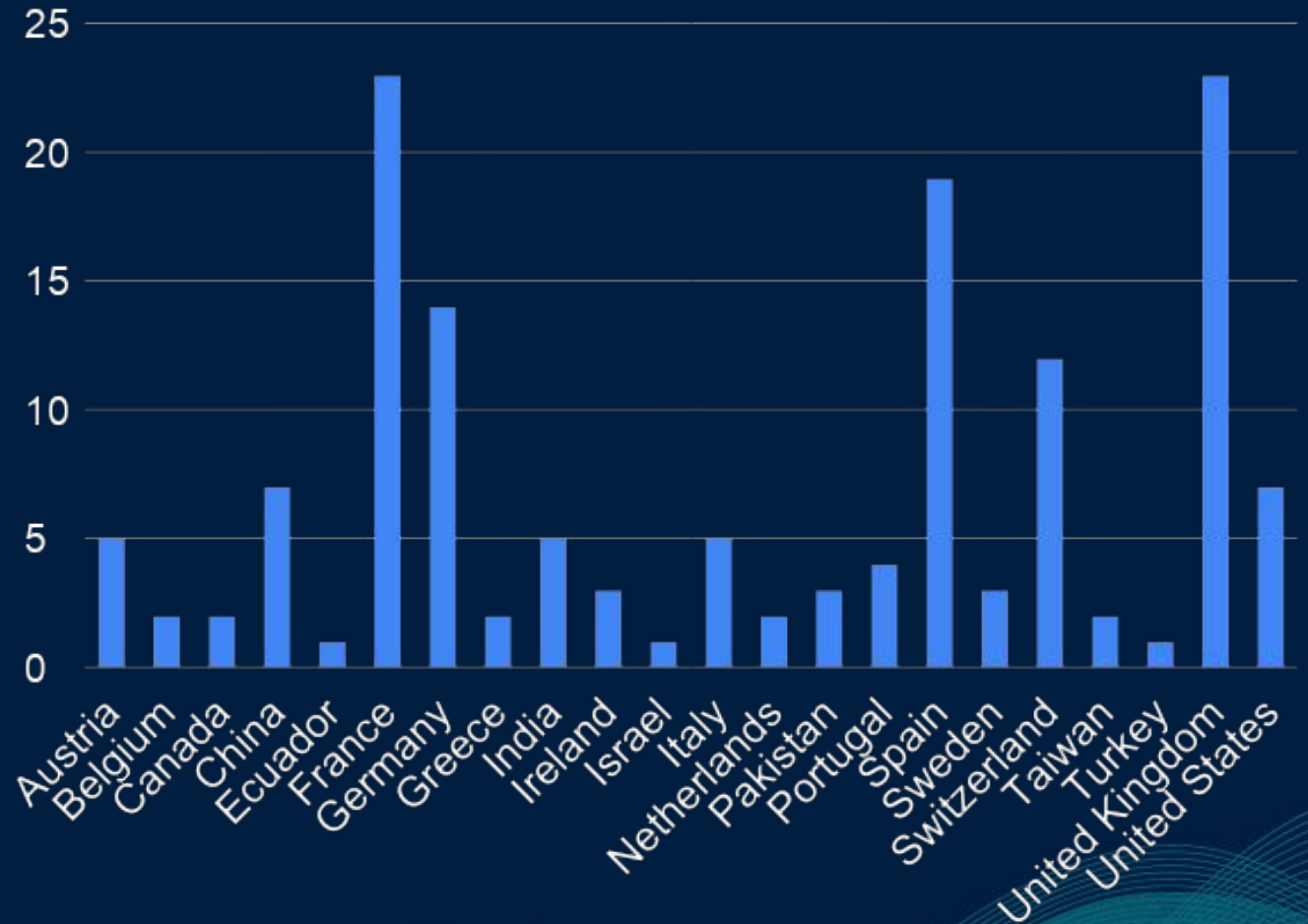
- Single track event
  - Keynotes: 30 min incl. Q&A
  - Technical/industry talks: 15 min incl. Q&A
  - *Speakers, please keep the time!*
- Poster exhibition area as a „*Marketplace of Ideas*“
  - Posters are available one complete event day.
  - Most technical and industry talks have a poster for further discussion.





# Submissions

- Submissions: #147
- Accepted as technical talks: #19
- Accepted as industry talks: #8
- Accepted as posters: #114
- Keynotes: #19
- Panels: #3





## Hot Topics 2023



# Big Thanks to the Program Committee

Jan Andersson (Frontgrade Gaisler)

Holger Blasum (Sysgo)

Alex Bradbury (Igalia)

Ramon Canal (UPC)

Denis Dutoit (CEA)

Angelo Garofalo (ETH Zürich)

Benedikt Gierlichs (KU Leuven)

Frederic Heitzmann (Tiempo Secure)

John Hengeveld (Intel)

Eyck Jentzsch (MINRES)

Nick Kossifidis (FORTH)

Marie-Minerve Louerat (LIP6)

Andreas Mauderer (Bosch)

Daniel Müller-Gritschneider (TU Munich), PC Co-Chair

Jérôme Quévremont (Thales)

Rihards Novickis (EDI, Latvia)

Katzalin Olcoz Herrero (UCM)

Borja Pérez Pavon (Univ. de Cantabria), Submission Chair

Olivier Savry (CEA)

Davide Schiavone (OpenHW Group)

Georg Sigl (TUM), PC Co-Chair

Jonathan Woodruff (Cambridge Univ.)

# Tuesday – Overview and Highlights

Keynotes

9:45 - Mark Himmelstein, RISC-V International  
10.00 - Rick O'Connor, OpenHW Group  
10.15 - Balaji Baktha, Ventana Micro Systems

**Spotlight: Commercial  
Application of RISC-V**

Keynotes

11.30 - Jon Taylor - Imperas Software Ltd  
11.45 - Brett Cline, Cudasip  
12.00 - Charlie Su, Andes Technology

Keynotes

14.00 - Roger Espasa, Semidynamics  
14.15 - Michael Gielda, Antmicro

Keynotes

16:30 - Franck Bernard, Bosch

**Spotlight: Security, Safety  
and Automotive**

Panel

17:30 - Automotive/Embedded Panel

Tue Posters

**Topics: Security, Embedded Apps & Publically funded project results**

# Wednesday – Overview and Highlights

Keynotes

9:00 - Luca Benini, ETH Zürich, Università di Bologna  
9:30 - Yungang Bao, China RISC-V Alliance (CRVA),  
Beijing Institute of Open-Source Chip (BOSC)  
10:00 - Dominic Rizzo, zeroRISC Inc, OpenTitan

**Spotlight: AI, HPC,  
Security, Android**

Keynotes

11:30 - Lars Bergstrom, Google

Keynotes

14:00 - Patrick Pype, NXP

Keynotes

16:45 - Matthew Xuereb, European Commission

**Spotlight: RISC-V's role in  
Europe**

Panel

17:15 - Panel on EU & RISC-V

Wed Posters

**Topics: AI, HPC, Open HW, Tools, Verification**

# Thursday – Program Highlights

Keynotes

9:00 - Philipp Tomsich, VRULL GmbH

9:30 - Thierry Collette, Thales R&T

**Spotlight: SW, ISA Extensions,  
Space and Open Hardware**

Keynotes

11:30 - Alex Bradbury, Igalia

Keynotes

14:00 - Roland Weigand, European Space Agency (ESA)

THU Posters

**Topics: ISA Extensions, RISC-V-based SoCs**



# Friday Workshops

- Open workshop: **EU Pilot: Towards European hardware & software for HPC.**
- Open workshop: **ORSHIN – Open-source ReSilient Hardware and software for Internet of thiNGs.**
- Open workshop: **RISC-V for Cloud Services.**
- Open Workshop (in Spanish): **Kickoff meeting for the second edition of Red-RISCV.**
- Open workshop: **Open Source Chips with Open Source Tools**
- Open workshop: **Open-Source Hardware Basic Training.**
- Open workshop: **OpenHW Group Meetings for Technical Working Groups & Task Groups.**

*Need separate registration for Friday attendance*



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# Venue Overview

- **Badge**

- Allergies / Intollerances / Special Diets

- Useful Information

- Up to 3 removable tickets

- Present to the attendees
- Social event
- Friday lunch box

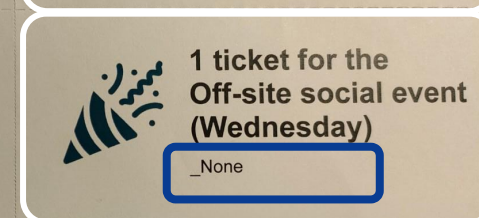
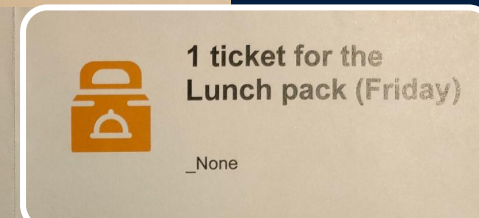
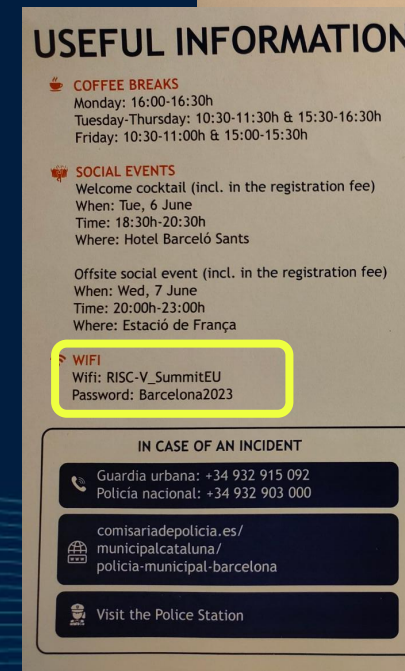
- + 2 single TMB tickets (Tren/metro)

2x



- Keep your badge visible at all time!!!

- RISC-V Summit Wifi **RISC-V\_SummitEU**





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# Venue


- **Registration Desk / Help desk**
  - Registration
  - Support
  - Students check-in point
  - Diamond sponsors meeting room tracking
- **Volunteers & Staff**
  - Registration desk
  - Plenary
  - Expo and Poster Areas
  - Meeting rooms
- **Auxiliary Screens out of the Expo and Plenary**
  - Plenary Program
  - Expo & Poster area organization (sponsors, posters, demo theater)



# Exhibition & Poster Areas

- **Expo & Poster areas** □ 5 min before the end of each break we will make ring a bell
  - + Food stations (coffee and lunch breaks)

- **MR07-08**

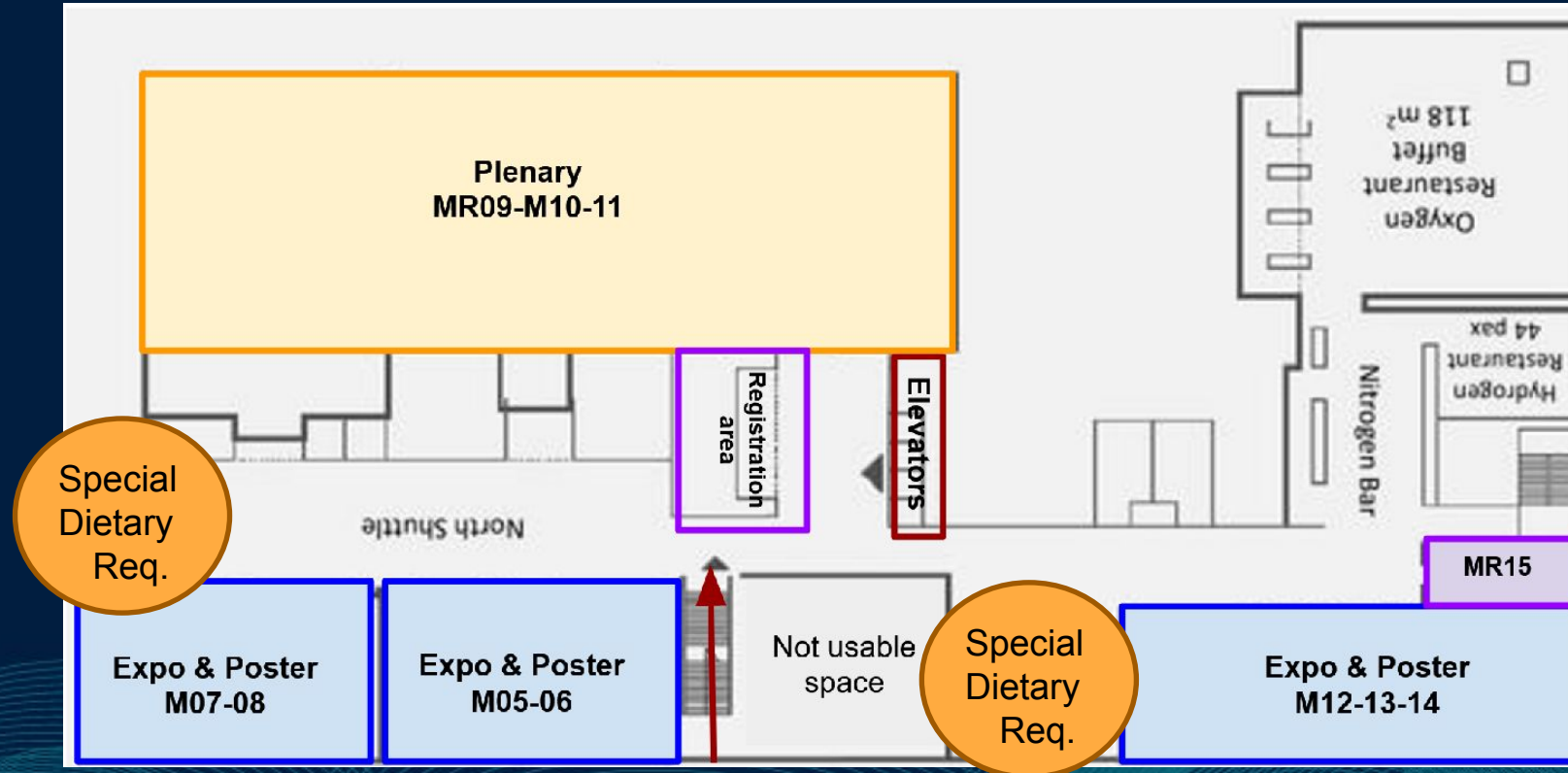
- Giveaway delivery 
- Posters 1 – 10
- Booths 1 – 7

- **MR05-06**

- Posters 11 – 20
- Booths 8 – 15

- **MR12-13-14**

- Posters 21 – 43
- Booths 16 – 24
- Demo Theater

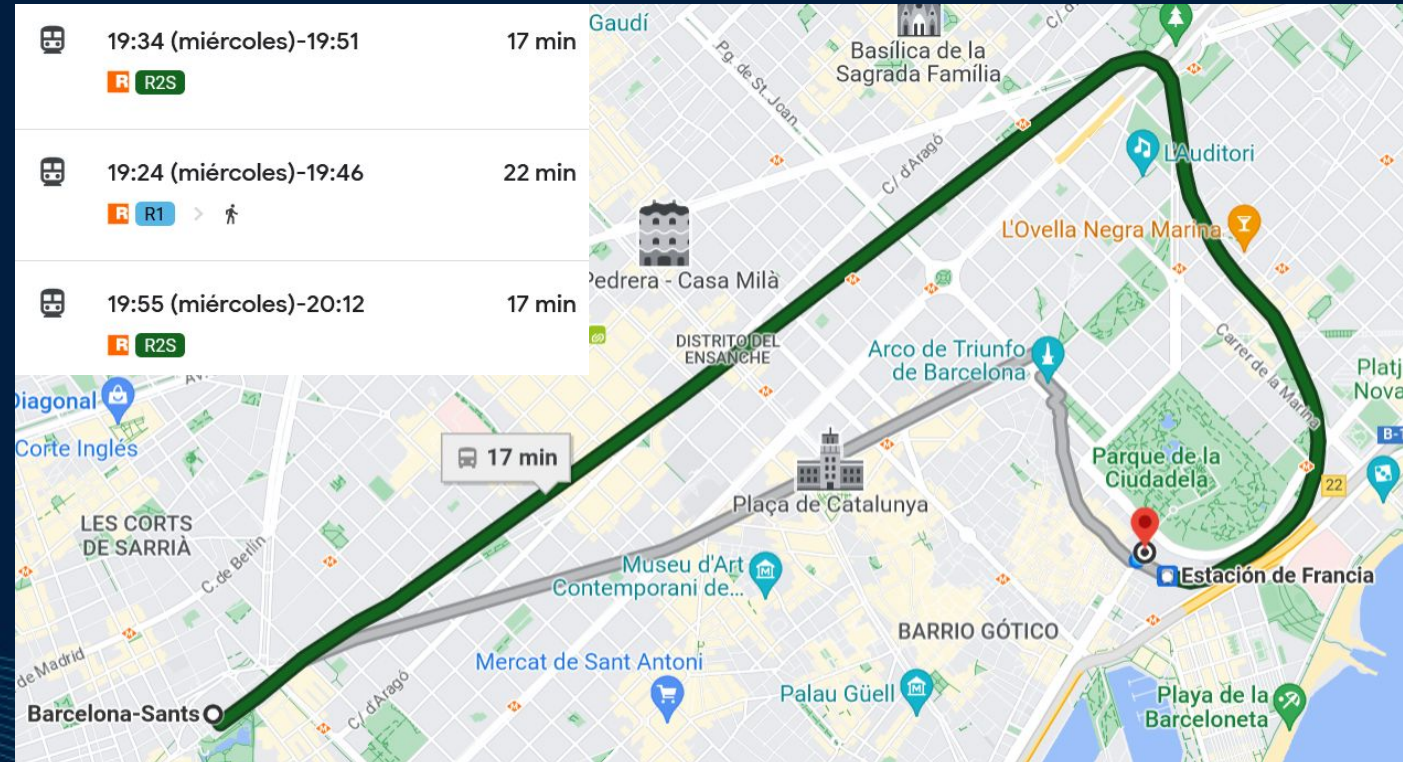




# Social Event(s)

- Tuesday: Welcome reception
  - Where? On-site: Expo & Poster areas
  - Time? After the last session (18:30 – 20:30)
- Wednesday: Social Event
  - Where? Off-site: Estació de França  
<http://restauranteestaciondefranca.com/>
  - Time? 20:00 – 23:00
  - How to get there?
    - 2x single train/metro tickets
      - Train (RS2)
      - Metro (L5>>L4 / L1>>L4)
      - Bus (D20)

By train



# After the event



RISC-V 2023 Summit Europe  
5-9 June 2023. Barcelona, Spain

## CERTIFICATE OF ATTENDANCE

The RISC-V Organising Committee certifies that

<Name, Surname>

coming from

European Research Centre

has attended the

RISC-V 2023 Summit Europe

hold from 5-9 of June 2023, at the Hotel Barceló Sants in Barcelona, Spain



RISC-V 2023 Summit Europe  
5-9 June 2023. Barcelona, Spain

## Feedback





# THANK YOU SPONSORS

DIAMOND



GOLD



SILVER



# Thursday – Program Highlights

- Spotlight: ISA Extensions, Space and Open Hardware

Keynotes

9:00 - Philipp Tomsich, VRULL GmbH - SW-driven evolution of a uniquely modular and extensible ISA

9:30 - Thierry Collette, Thales R&T - 4 years of Open Source RISC-V at Thales

Keynotes

11:30 - Alex Bradbury, Igalia - Developments in LLVM-based toolchains and tooling for RISC-V

Keynotes

14:00 - Roland Weigand, European Space Agency (ESA) - RISC-V: a Rising Star in Space