



## SmallSat Payload Control & Data Processing: High-Reliability and High-Security with RISC-V

The **Control & Data Processing Unit (CDPU)** offers flexibility and reduced Time-to-Orbit for compact SmallSat instruments

- Flexibility at design-time
- In-orbit reconfigurability
- In-orbit edge computing (image processing,



encryption, <u>instrument autonomy</u>)
>5 years in-orbit life-time (LEO)

Trade-off: COTS | Rad.-tolerant | Rad.-hardened



CDPU base-line design: **FreNox RISC-V IP** (RV32IMA)

- SpW interface IP
- SpFi interface IP
- AXI interconnect infrastructure
- Accelerator support

High-Reliability & High-Security features: Lockstep

Error Detection and Correction





- Lightweight checkers (Hardware Trojans & SEE)
- FDIR handling
- PQC encryption & authentication
- Secure boot & reconfiguration

Implemented in **(RT) PolarFire FPGA** Successfully tested with 4Mpx camera head (3D PLUS)

CE dependency on platform

Challenges to address for NewSpace SmallSat





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