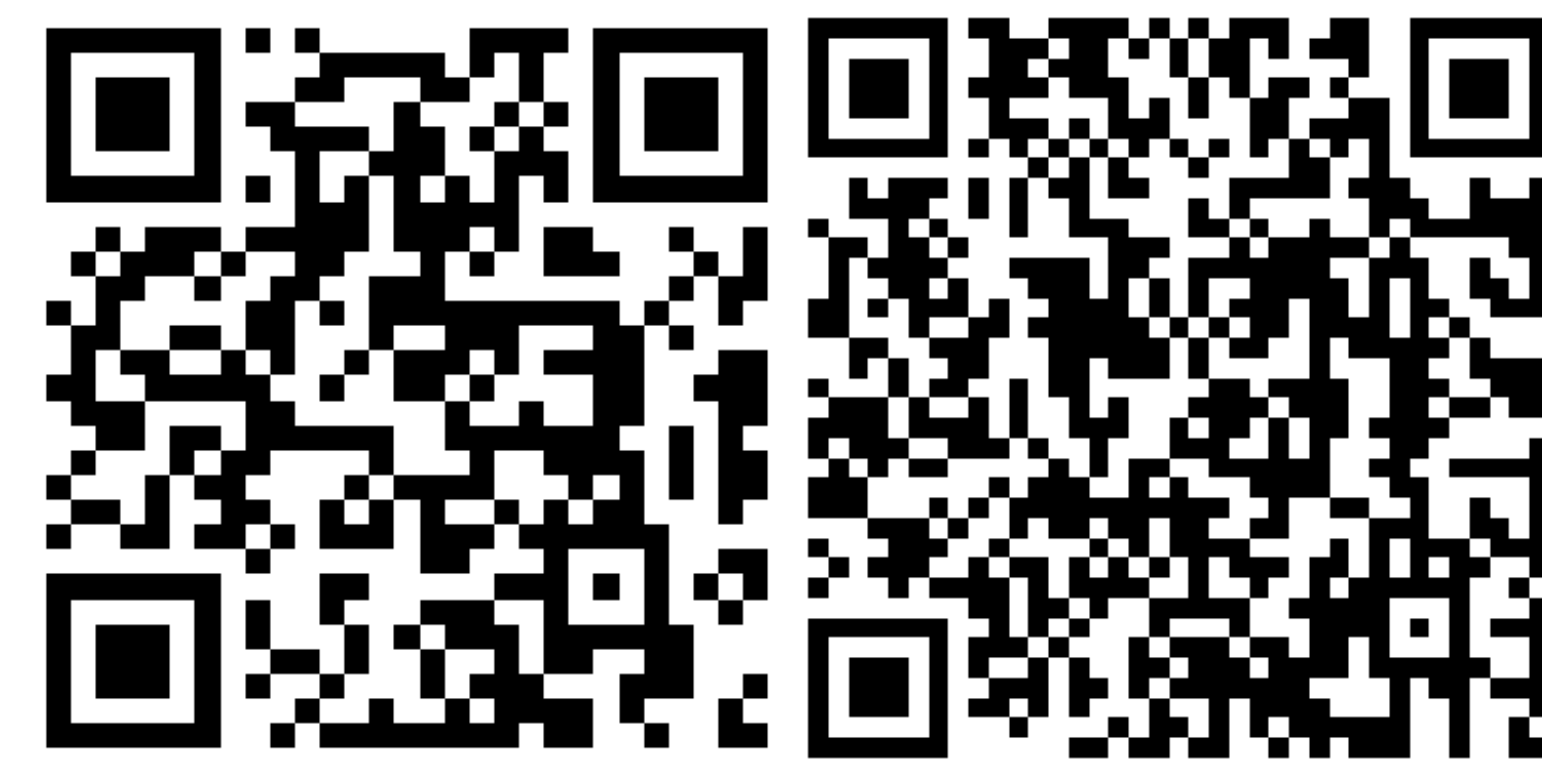


One Student One Chip

Start From Scratch

Create Your Own

RISC-V[®] Processor



Website

PPT

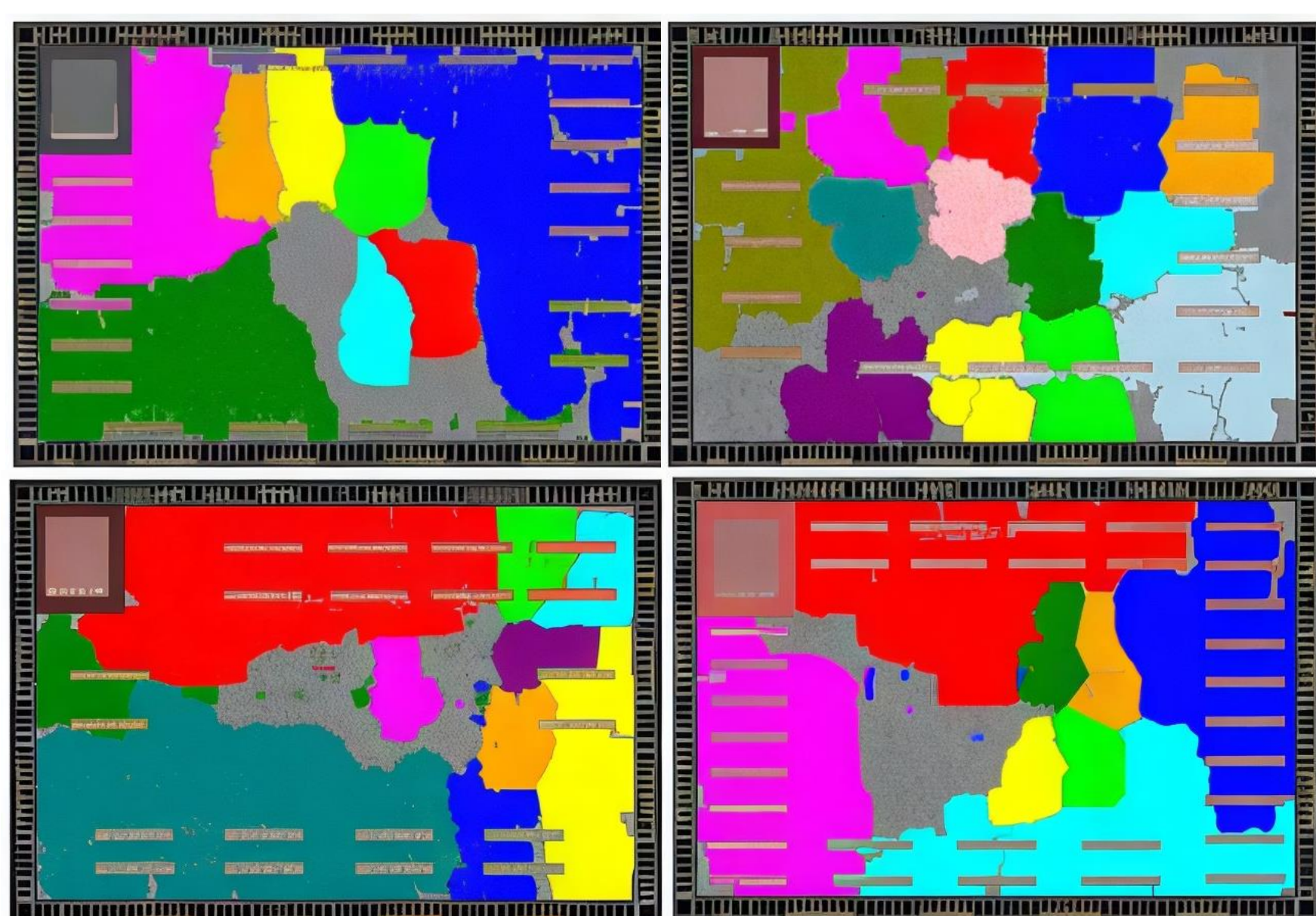
E-mail: ysyx@bosc.ac.cn

- Open Source and Free of Charge

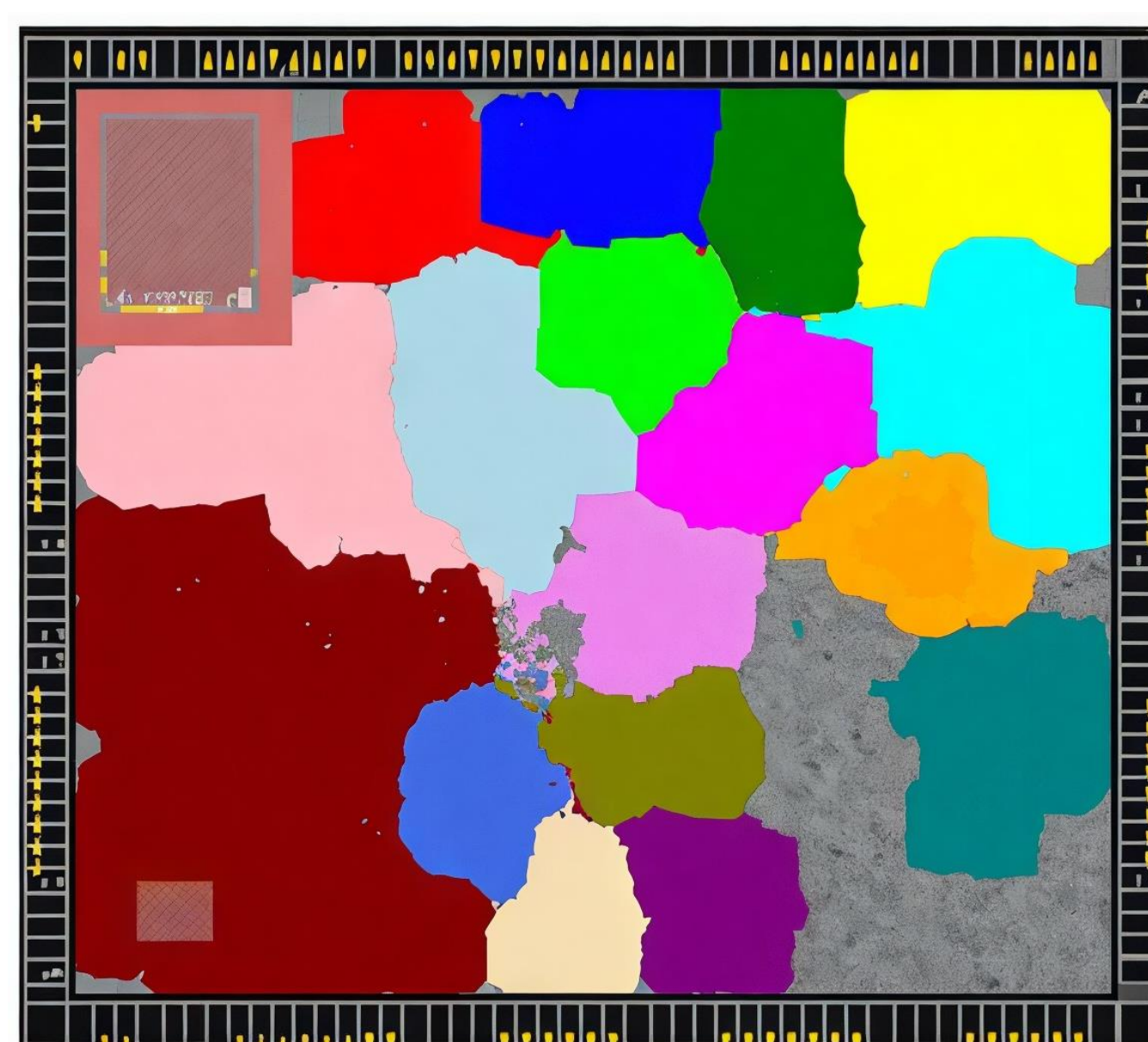
- SW-HW Co-design and Full Design Flow For Processor

- Sign Up Beyond **7000+** , Covering **350+** Universities

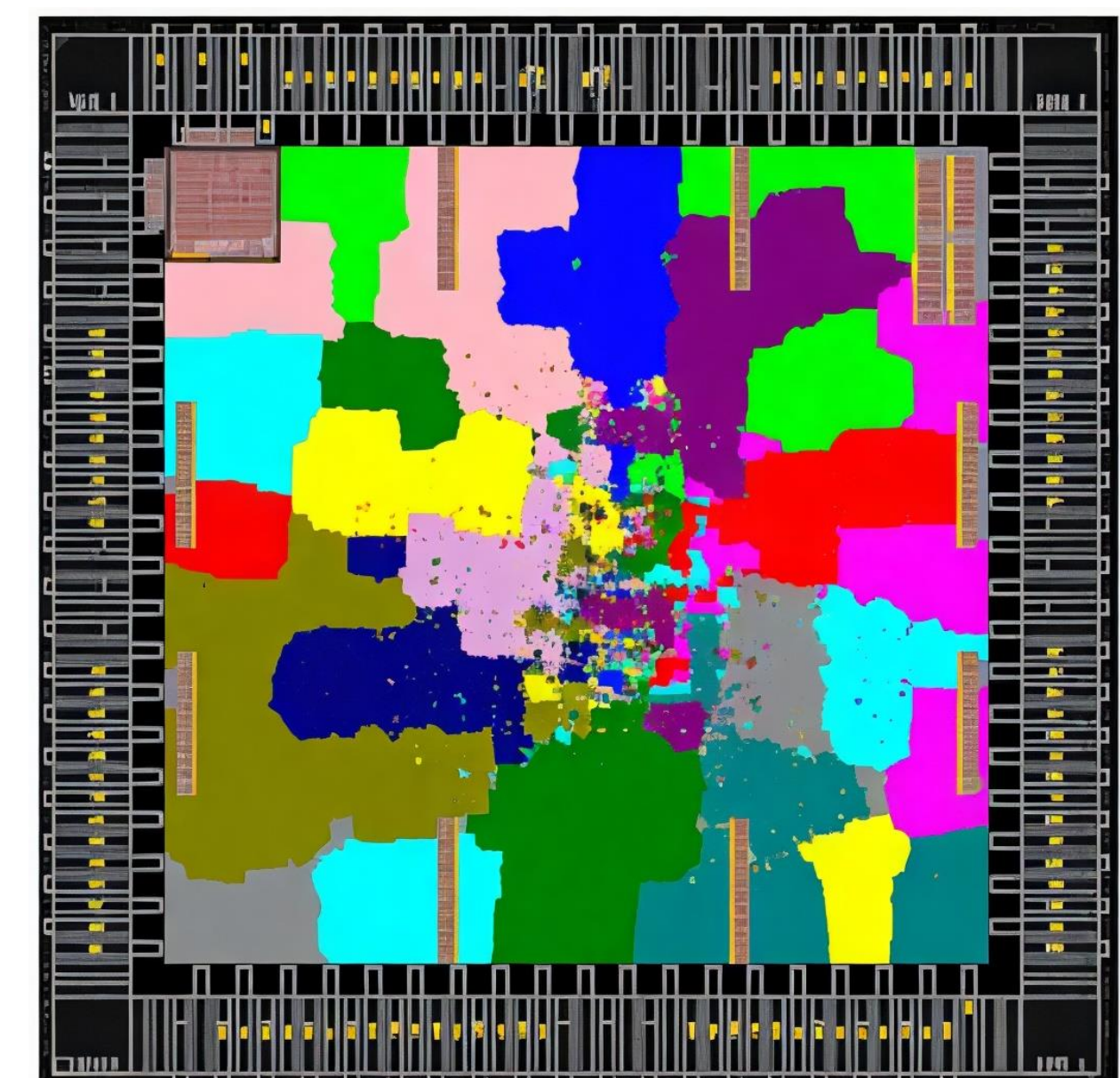
- **No Requisites For Enrollment**, Everyone is Welcome



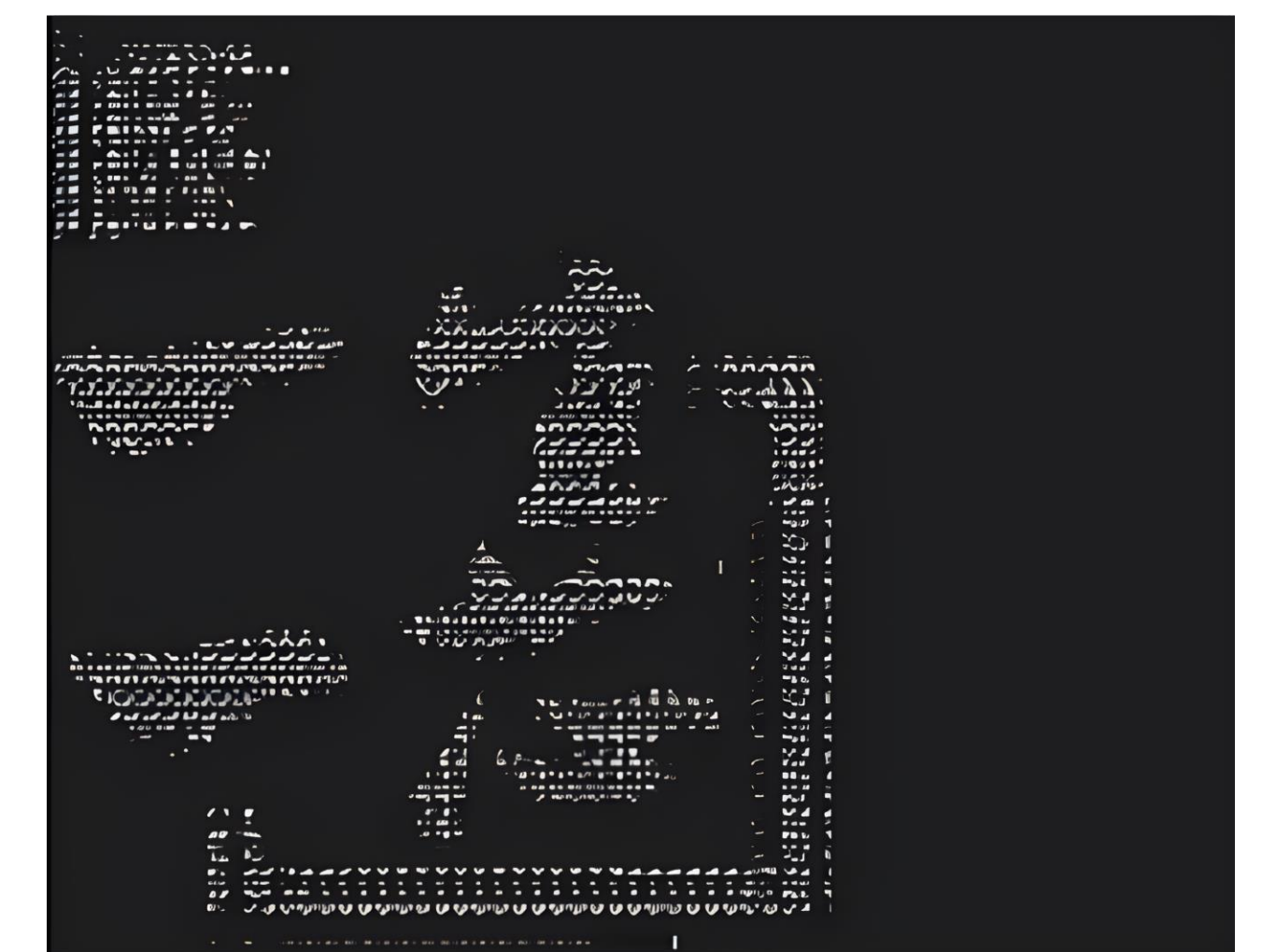
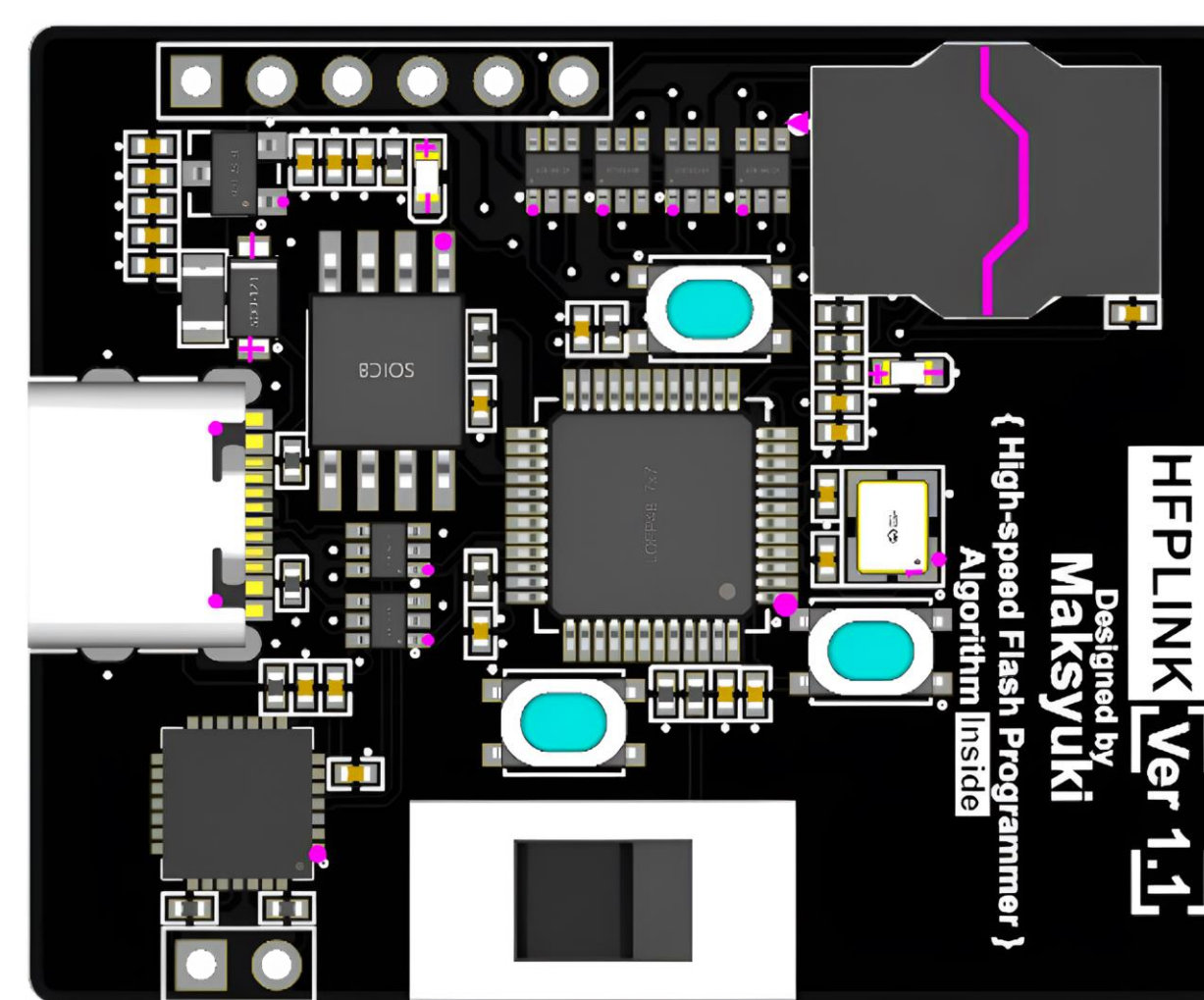
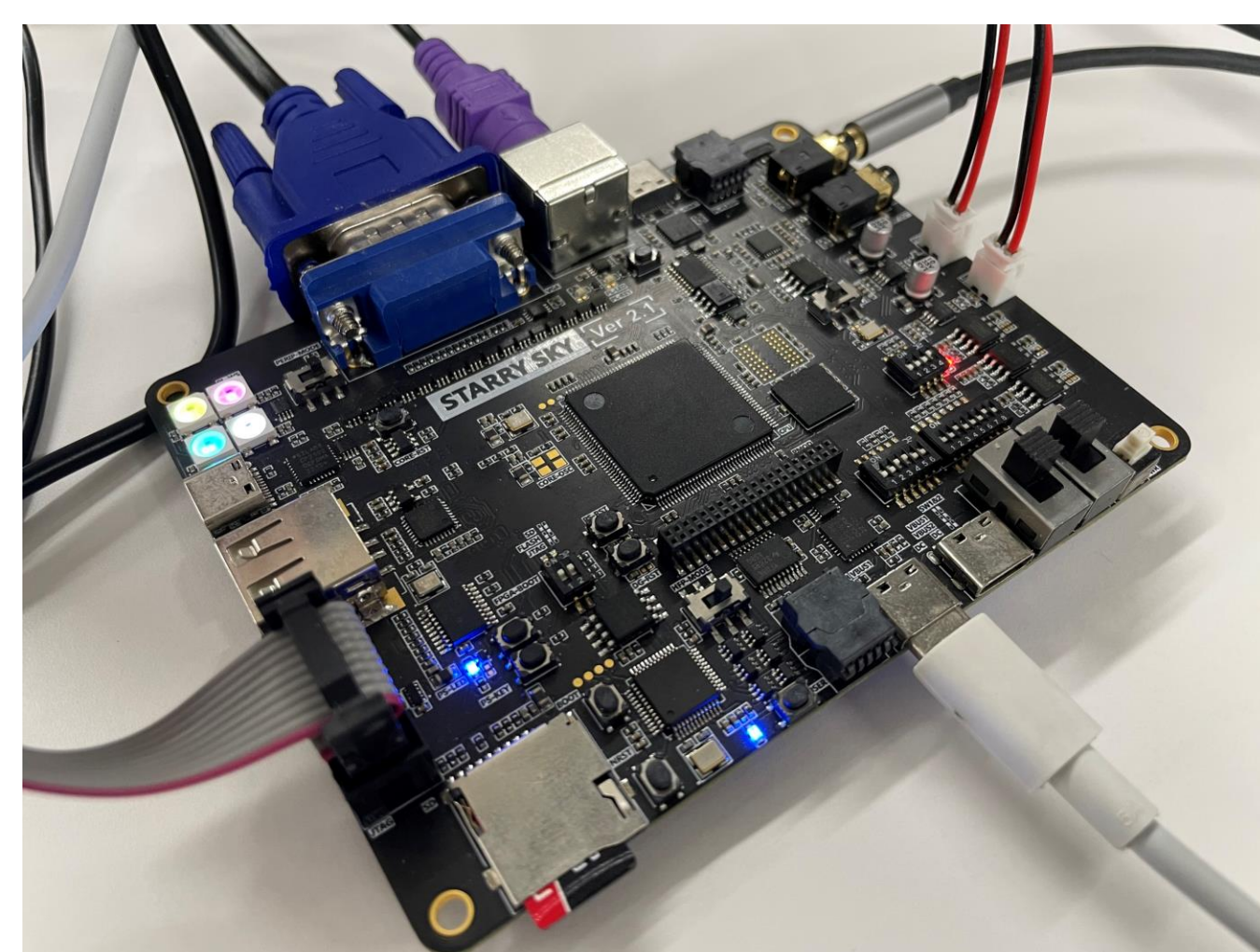
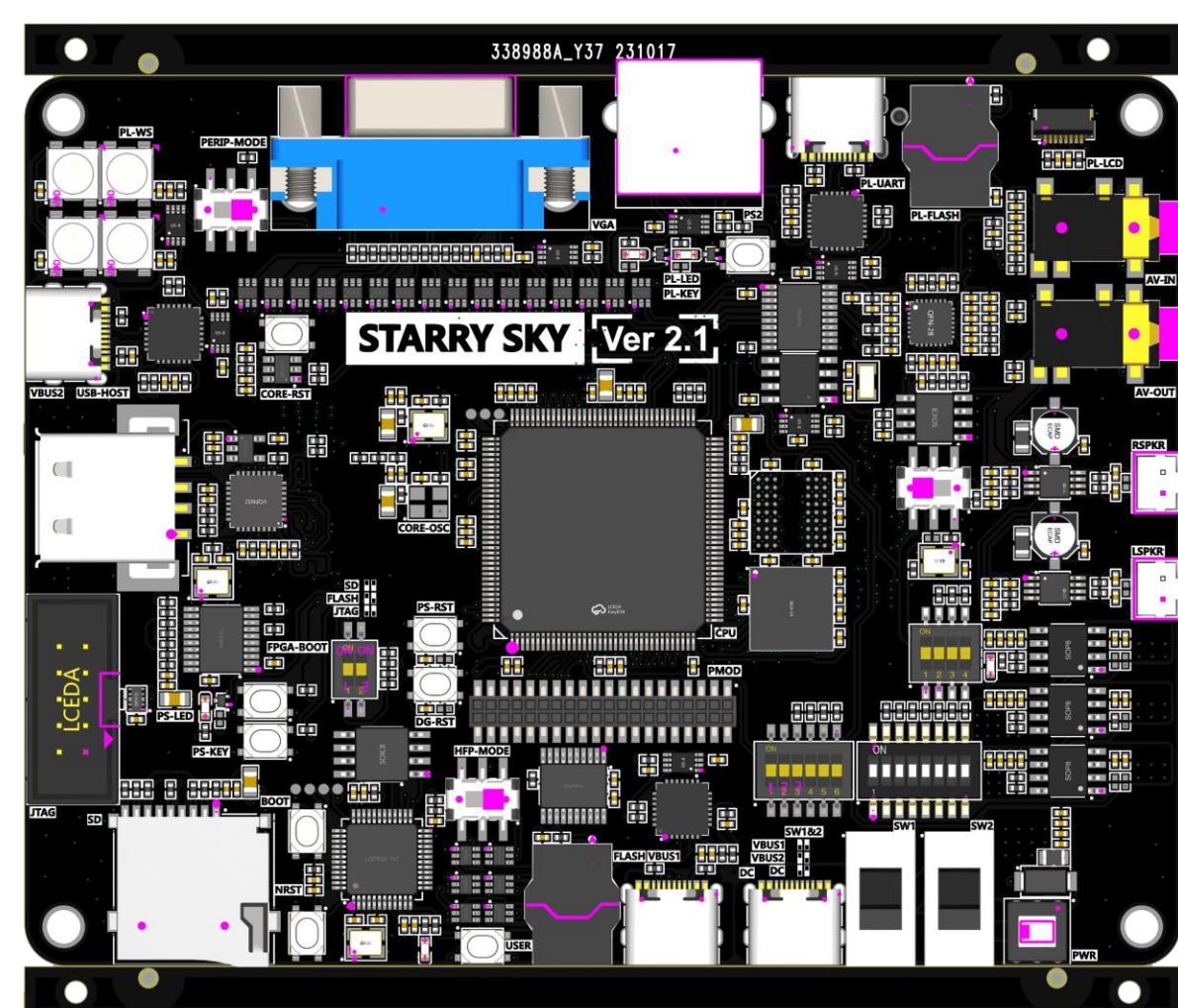
The 3rd Session of OSOC (1st Batch, 39 cores)



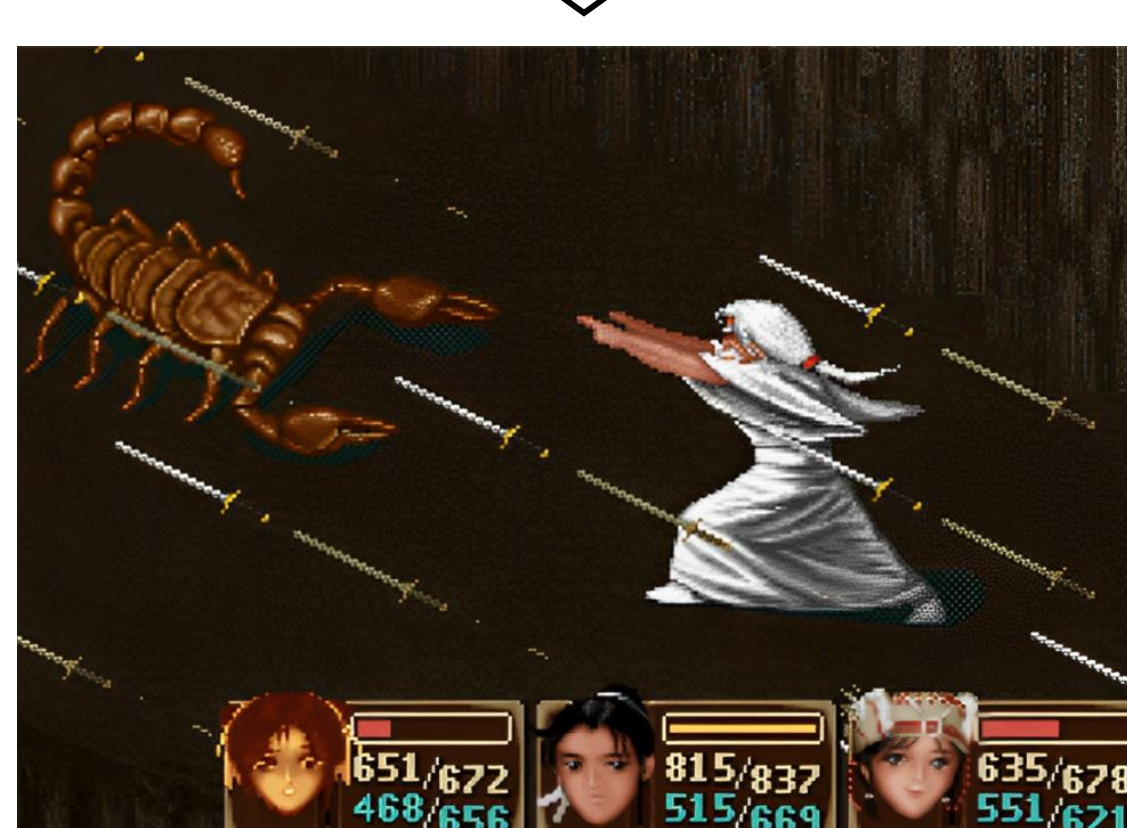
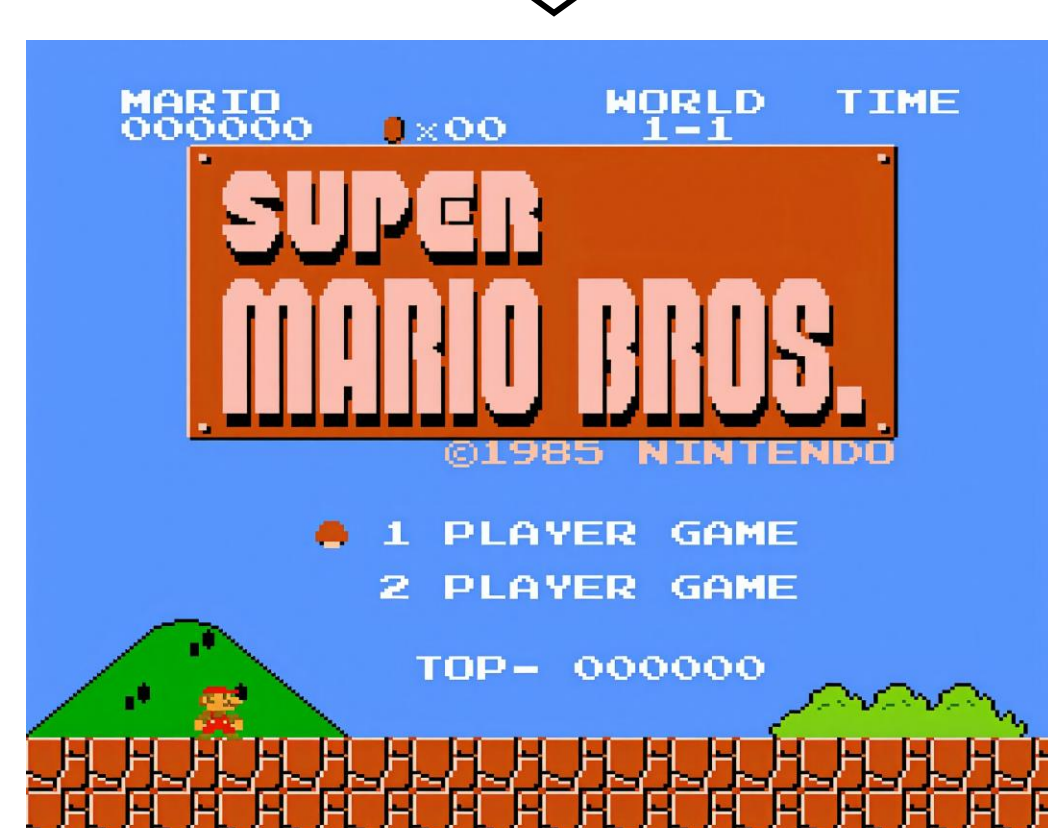
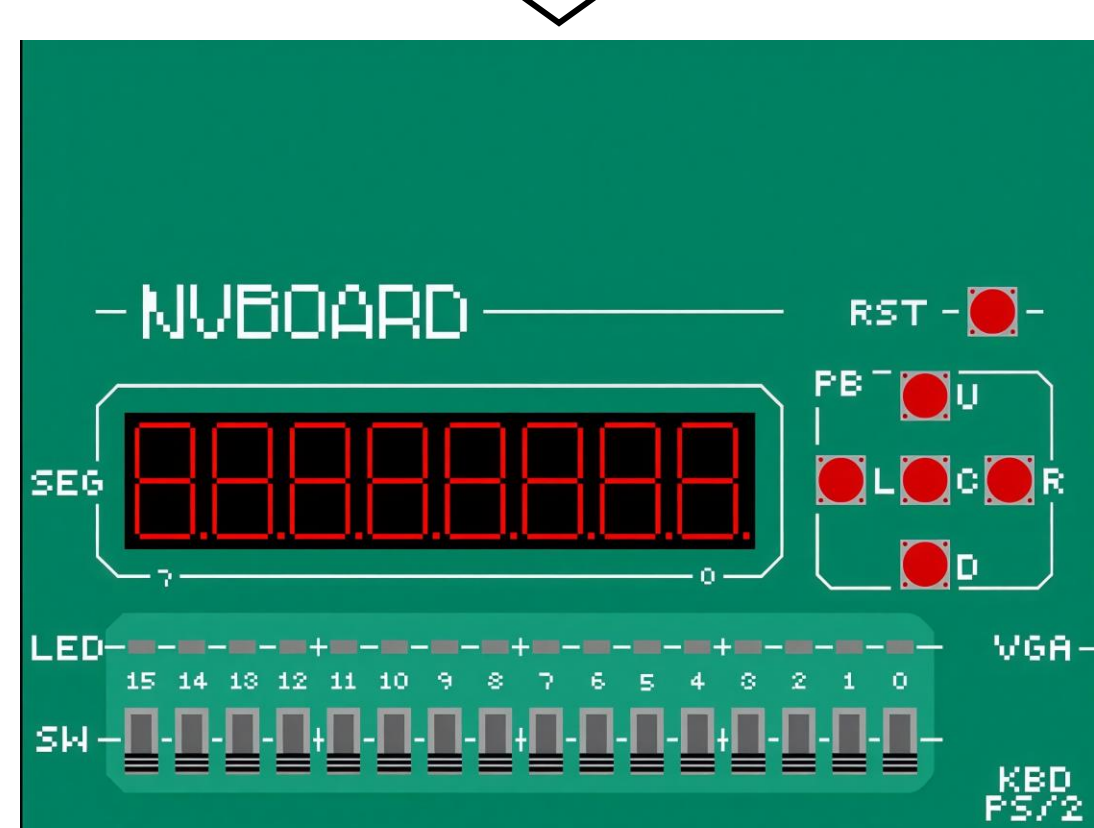
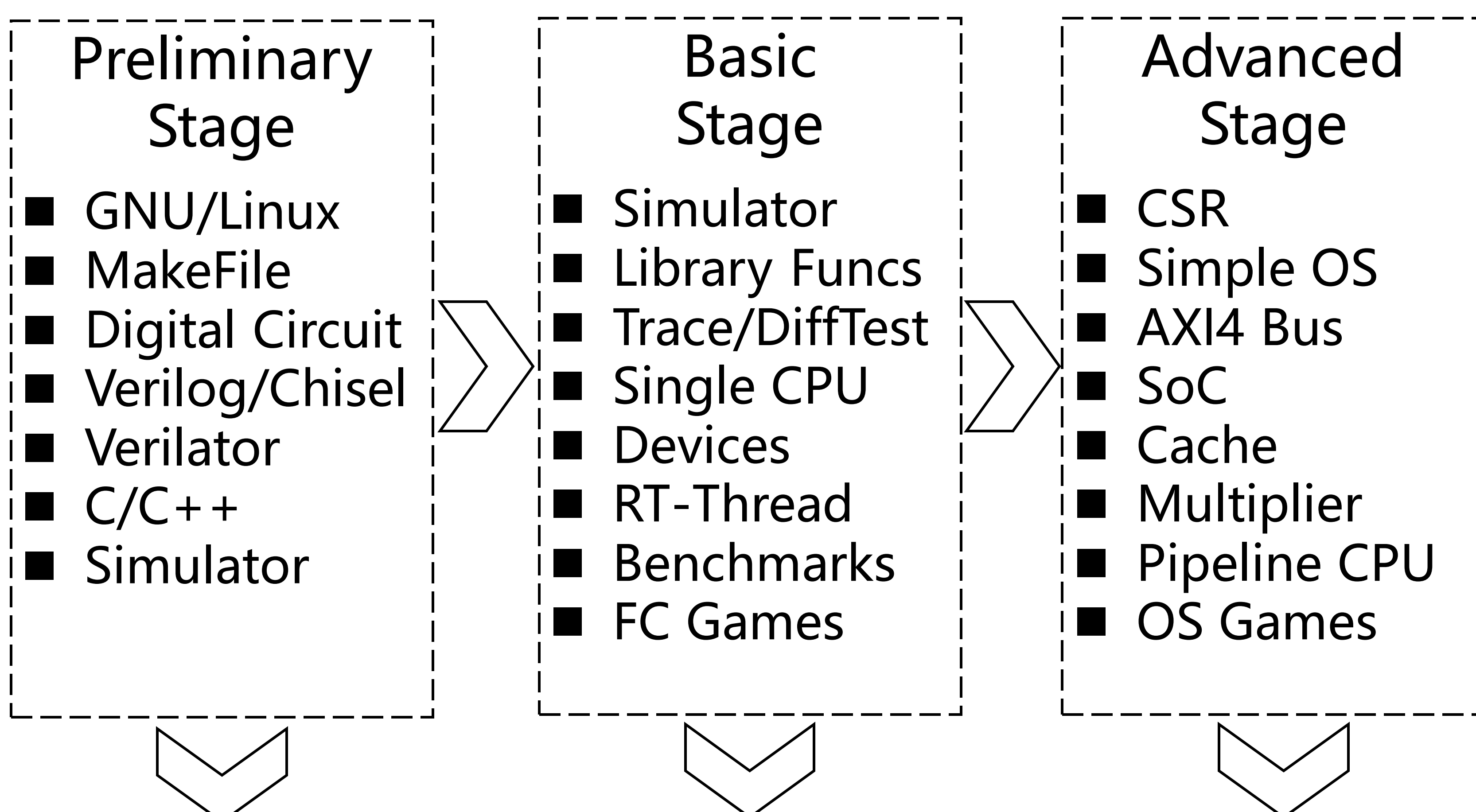
The 3rd Session of OSOC (2nd Batch, 9 cores)



The 4th Session of OSOC (1st Batch, 13 cores)



PCB Board Design&& Testing



Chips of OSOC