

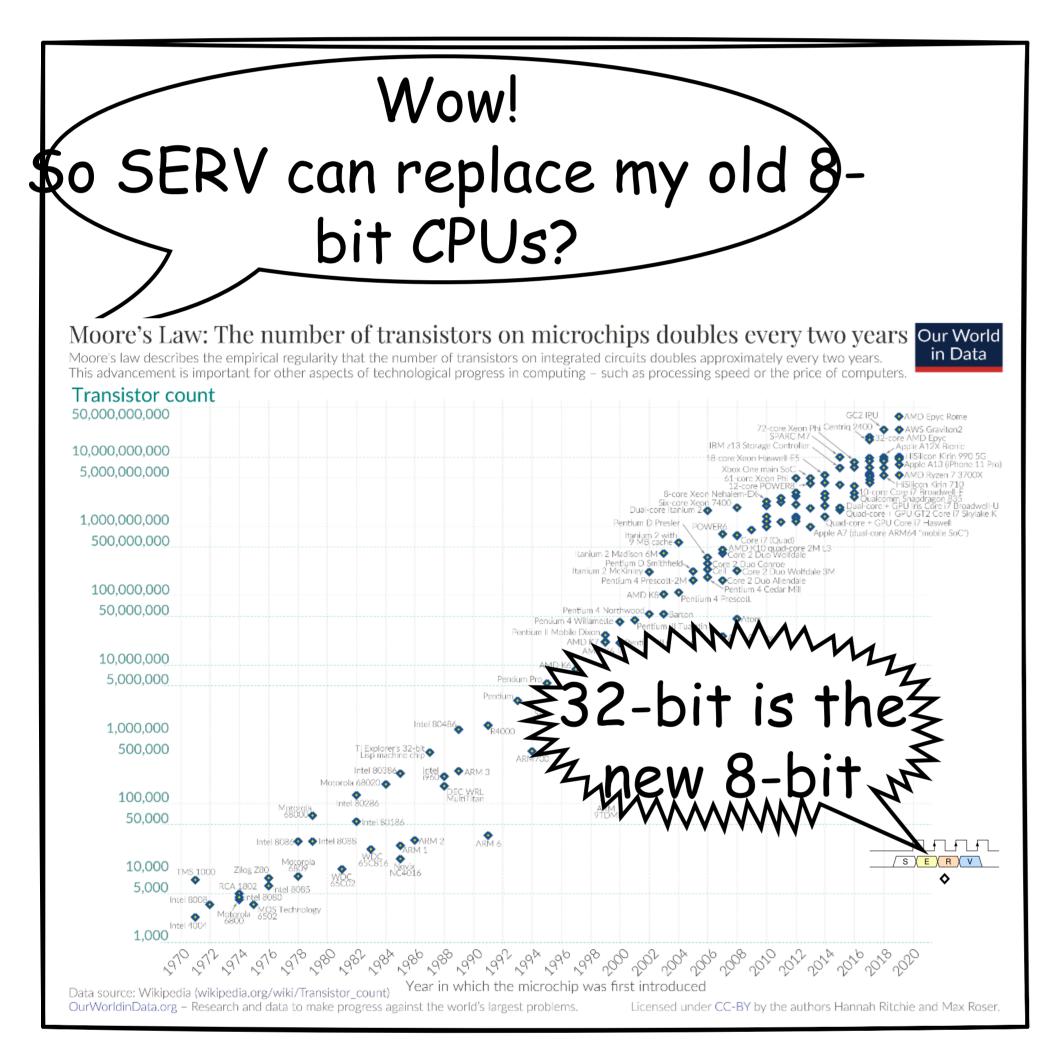
## SERV, QERV and HERV. Meet the world's smallest RISC-V cores

The award-winning SERV is the world's smallest RISC-V CPU		Ok, how small?
RISC-V CPU	SEDV fact chect	

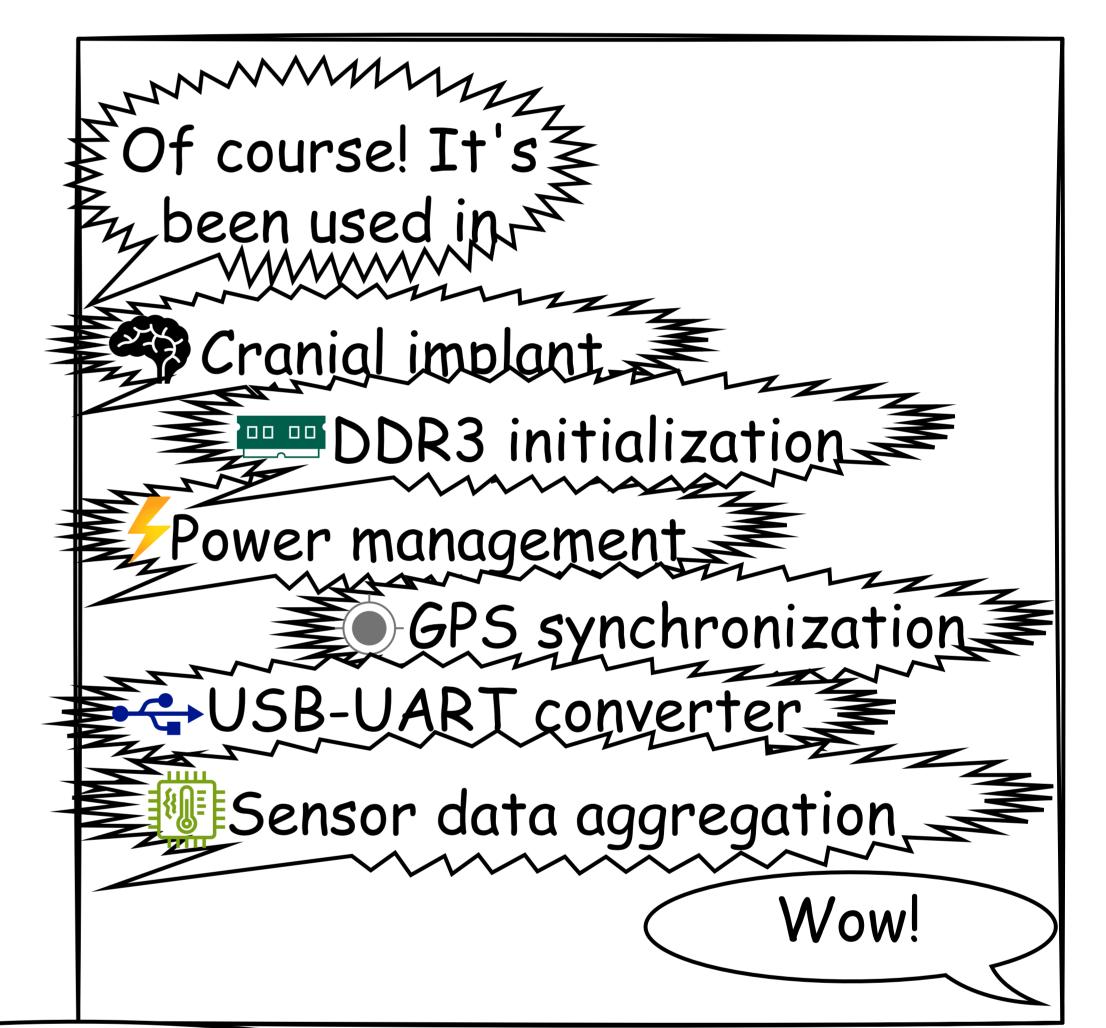


SERV TACT SNEET **RV32I** compliant Runs Zephyr ✓ M&C extensions ✓ ASIC&FPGA-proven Formally yerified Did I mention that it is also really really small? AAAA Daiwa

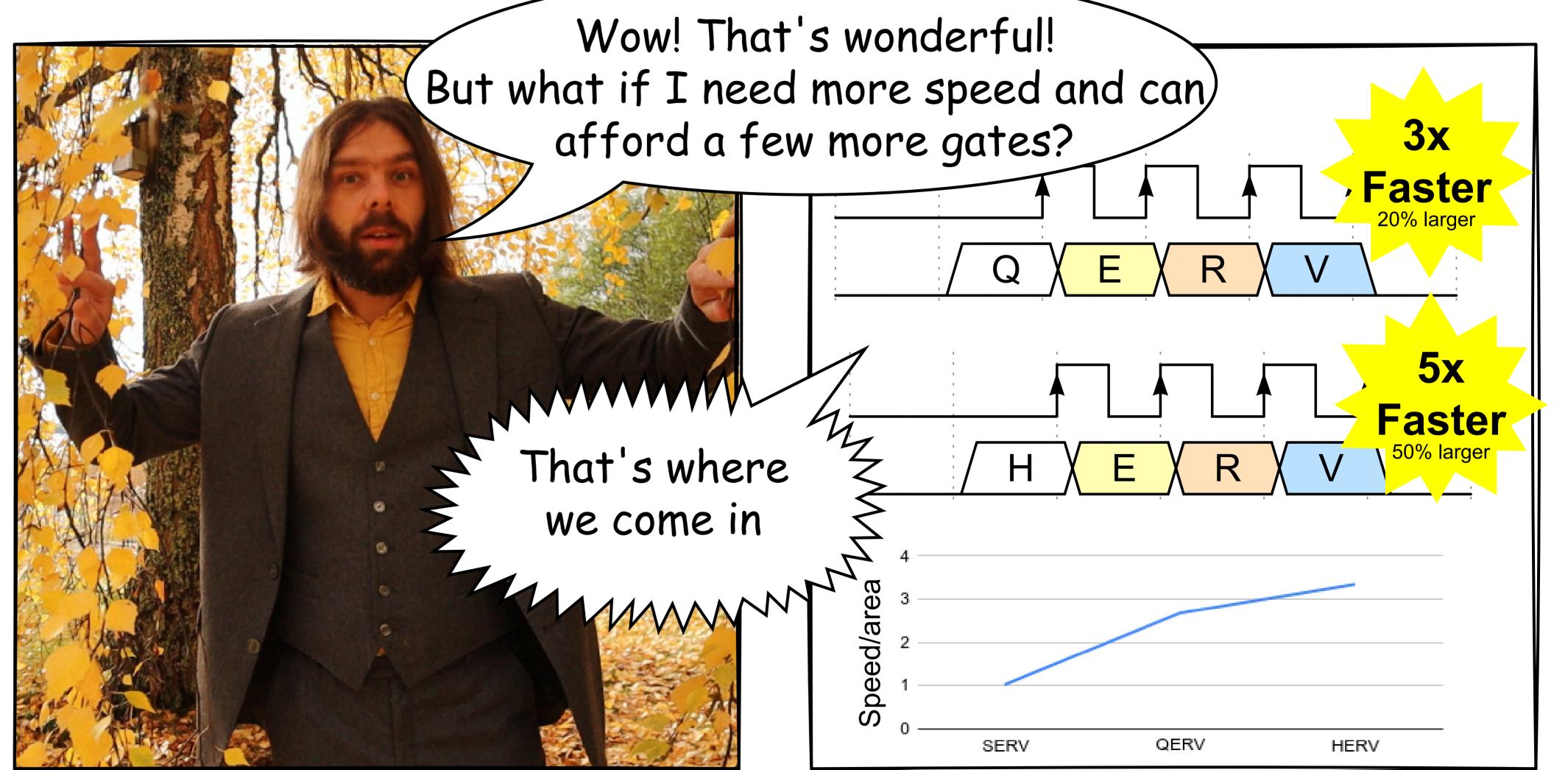
	Lattice iCE40	196LUT 164FF
	Altera Cyclone 10LP	239LUT 164FF
	AMD Artix7	125LUT 164FF
njiro	SkyWater 130nm	7901um2 2.1kGE











Want to try out SERV already today? Check out the project site



Want to discuss a commercial license for SERV or just have a question? Or perhaps linterested in other RISC-V services? Talk to us at Qamcom



Did you know you can fit 10000 SERV cores into a single FPGA.

How many cores can you fit into your FPGA? Find out with the CoreScore project

