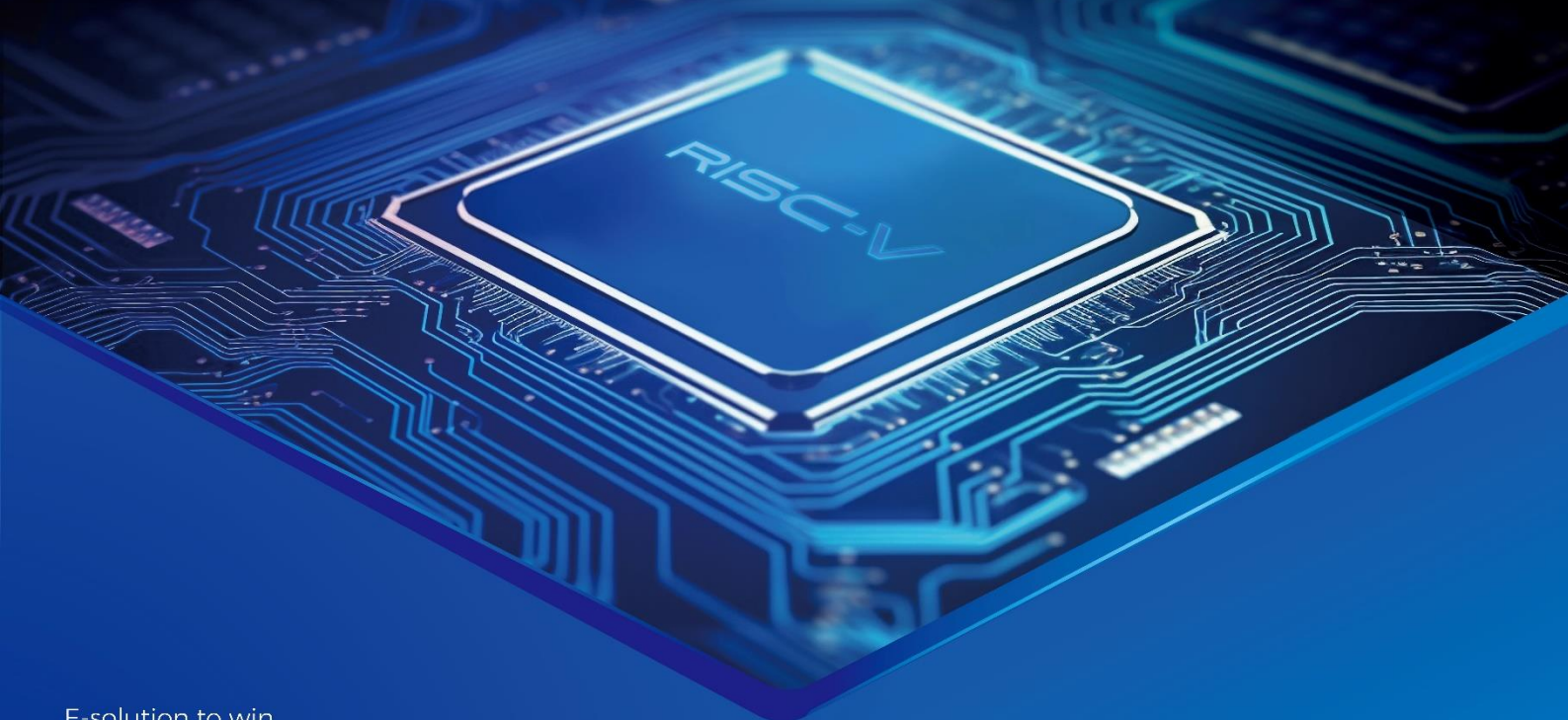
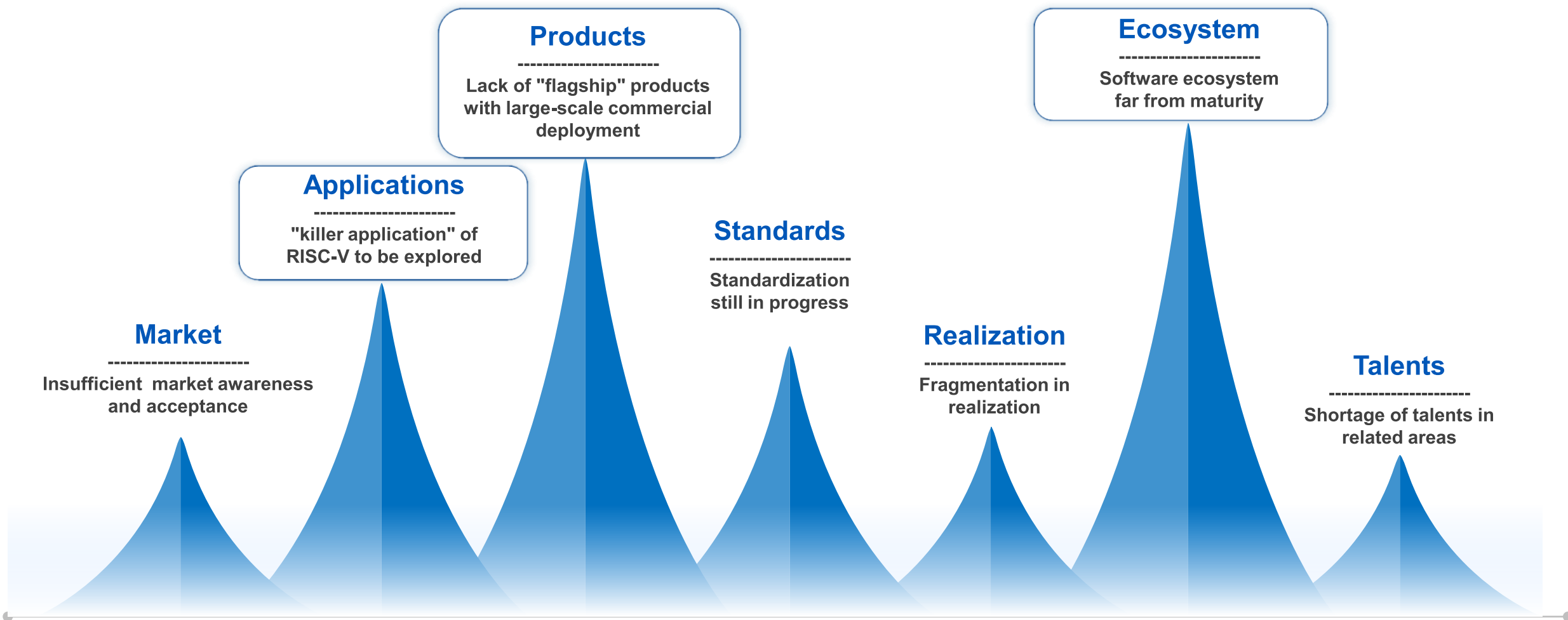


Enabling the Next Phase of RISC-V: Product Innovation and Scalable Solutions



北京奕斯伟计算技术股份有限公司
Beijing ESWIN Computing Technology Co., Ltd.

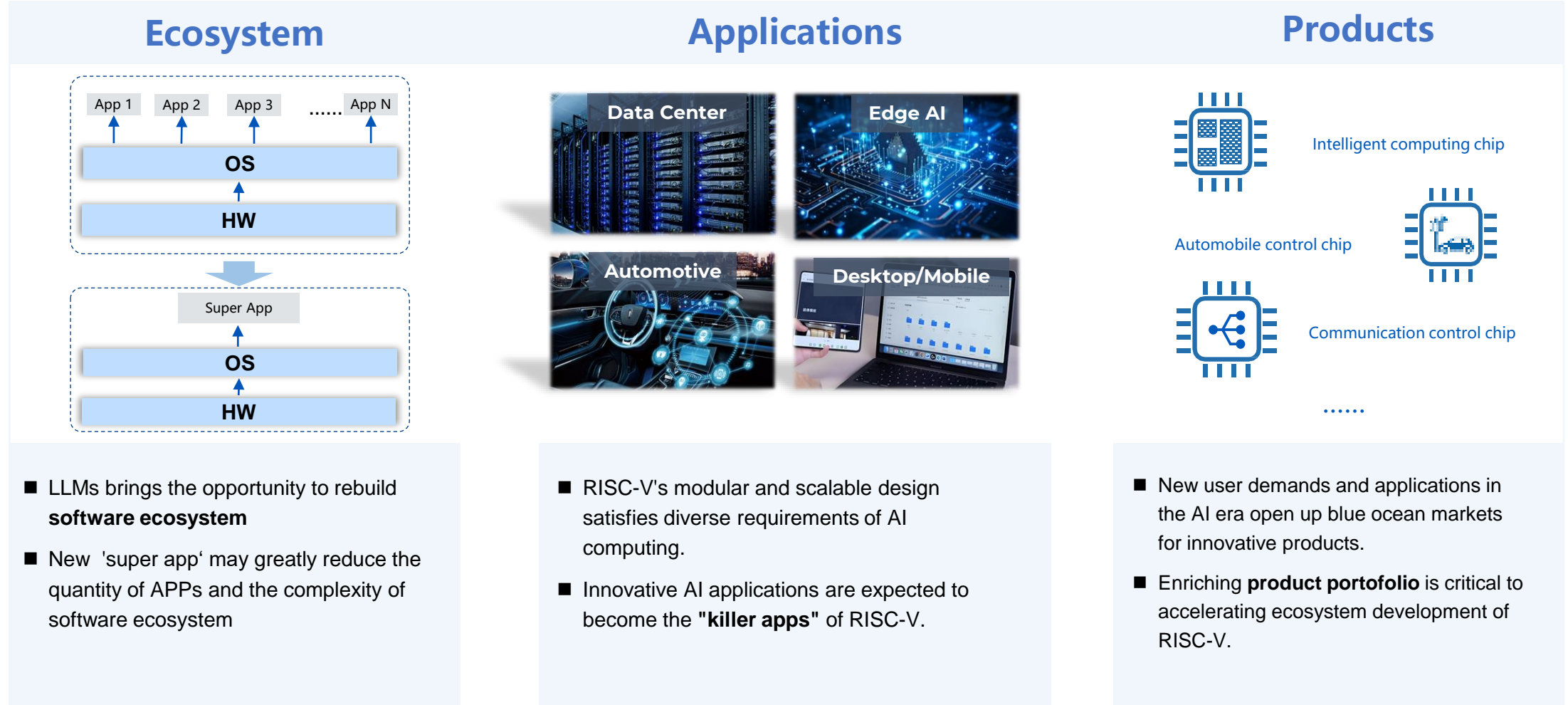
- I. RISC-V: Challenges & Opportunities**
- II. ESWIN Computing's Product Innovation**
- III. Enabling the Next Phase of RISC-V**



The main challenges faced by RISC-V (RISC-V Summit China 2022)

I. RISC-V: Opportunities

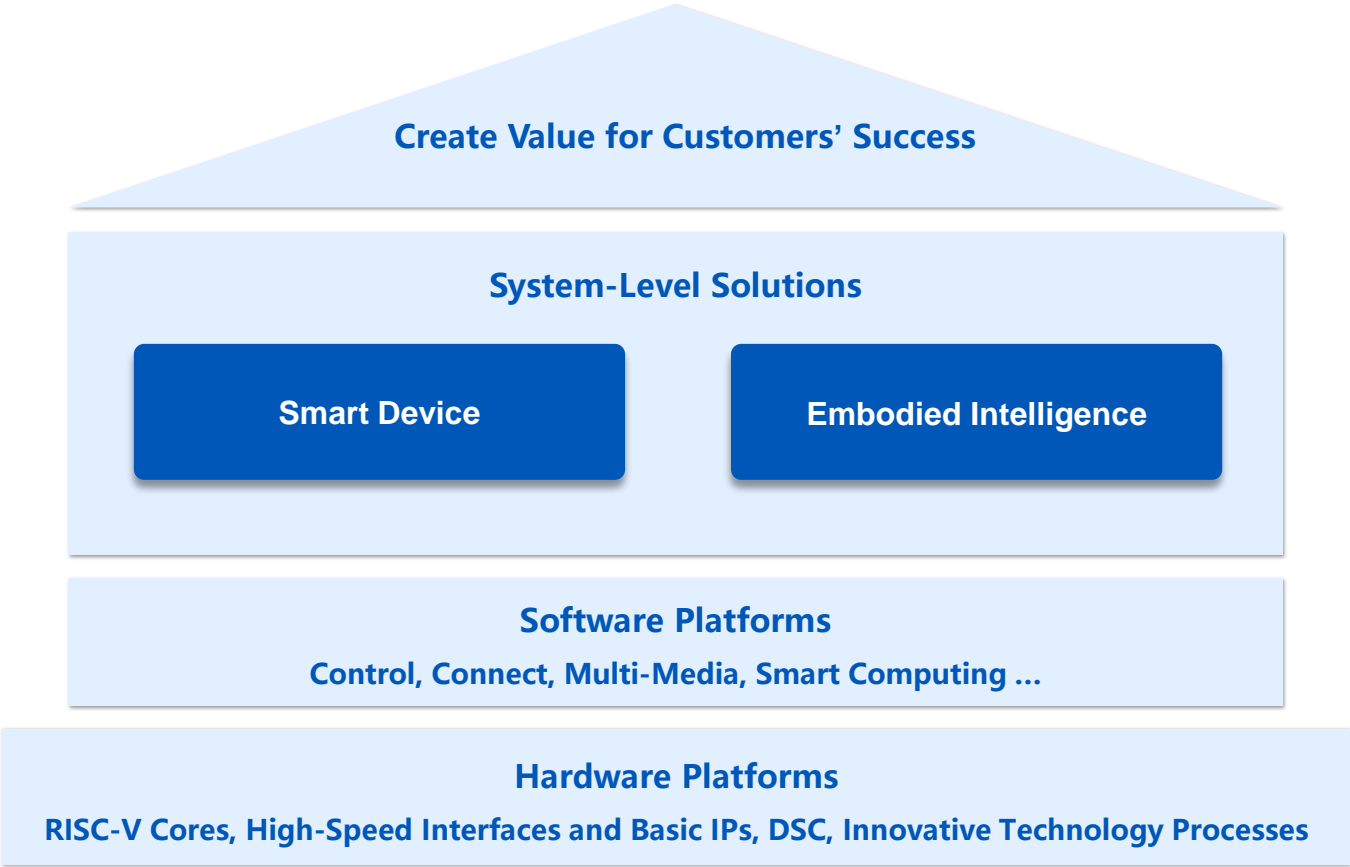
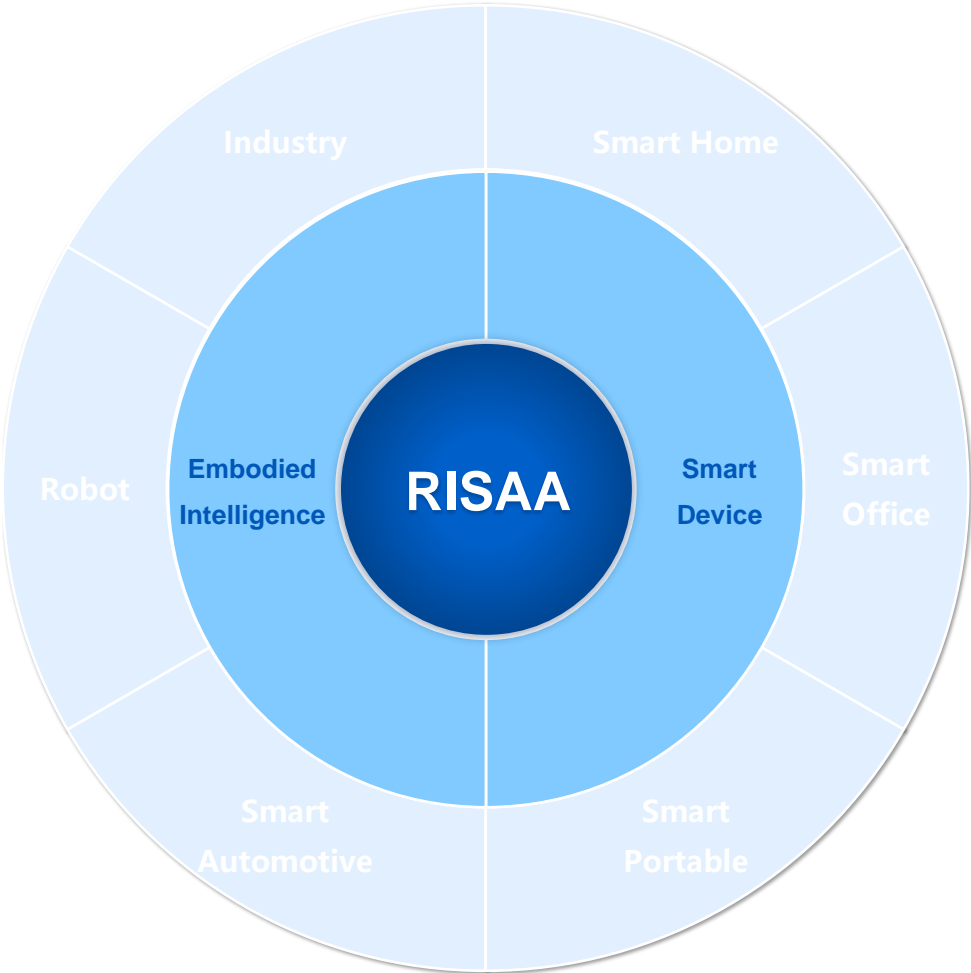
- The emergence of LLM such as ChatGPT and DeepSeek has provided RISC-V with good opportunities to address the challenges of ecosystem, applications, and products



- I. RISC-V: Challenges & Opportunities
- II. ESWIN Computing's Product Innovation**
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II. ESWIN Computing's Product Innovation — Business Orientation

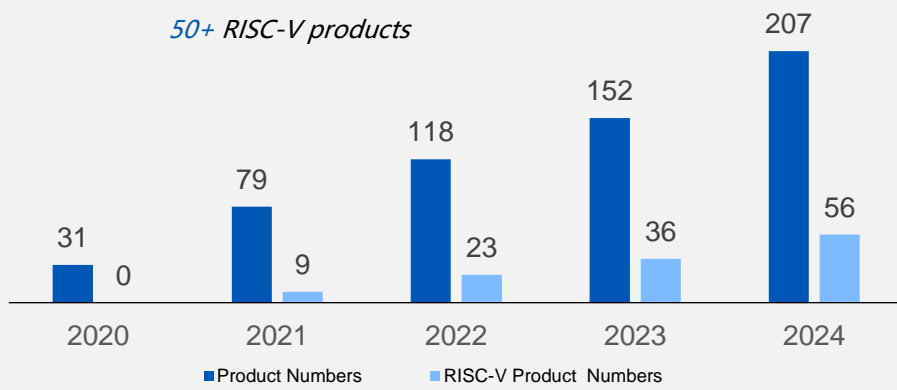
A Provider of Intelligent System-Level Solutions



II. ESWIN Computing's Product Innovation — Achievements

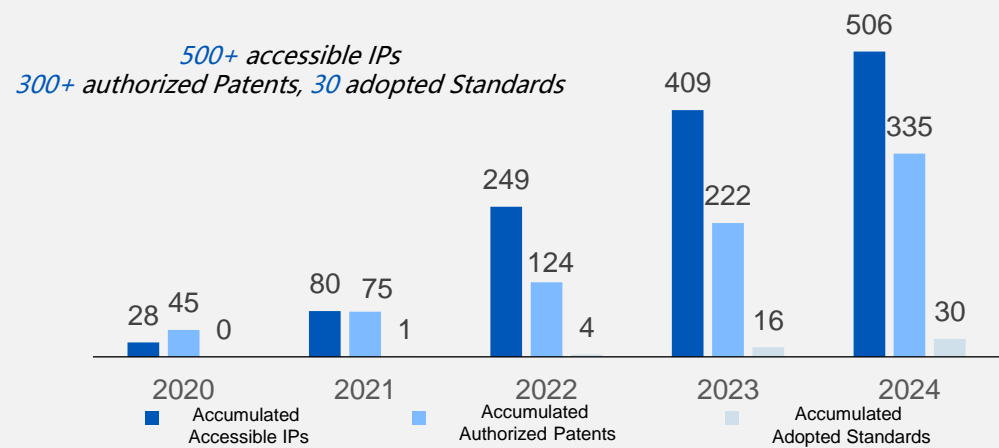
Products

200+ Products



Technology

IP/Patents/Standards



Industry Roles

Premier Member



RISC-V International (RVI)

General Member



RISC-V Software Ecosystem(RISE)

Rotating Chairman



RISC-V Working Committee of China
Electronics Standardization
Association(RVEI)

Initiating Unit



Beijing Open Source Chip Research
Institute (BOSC)

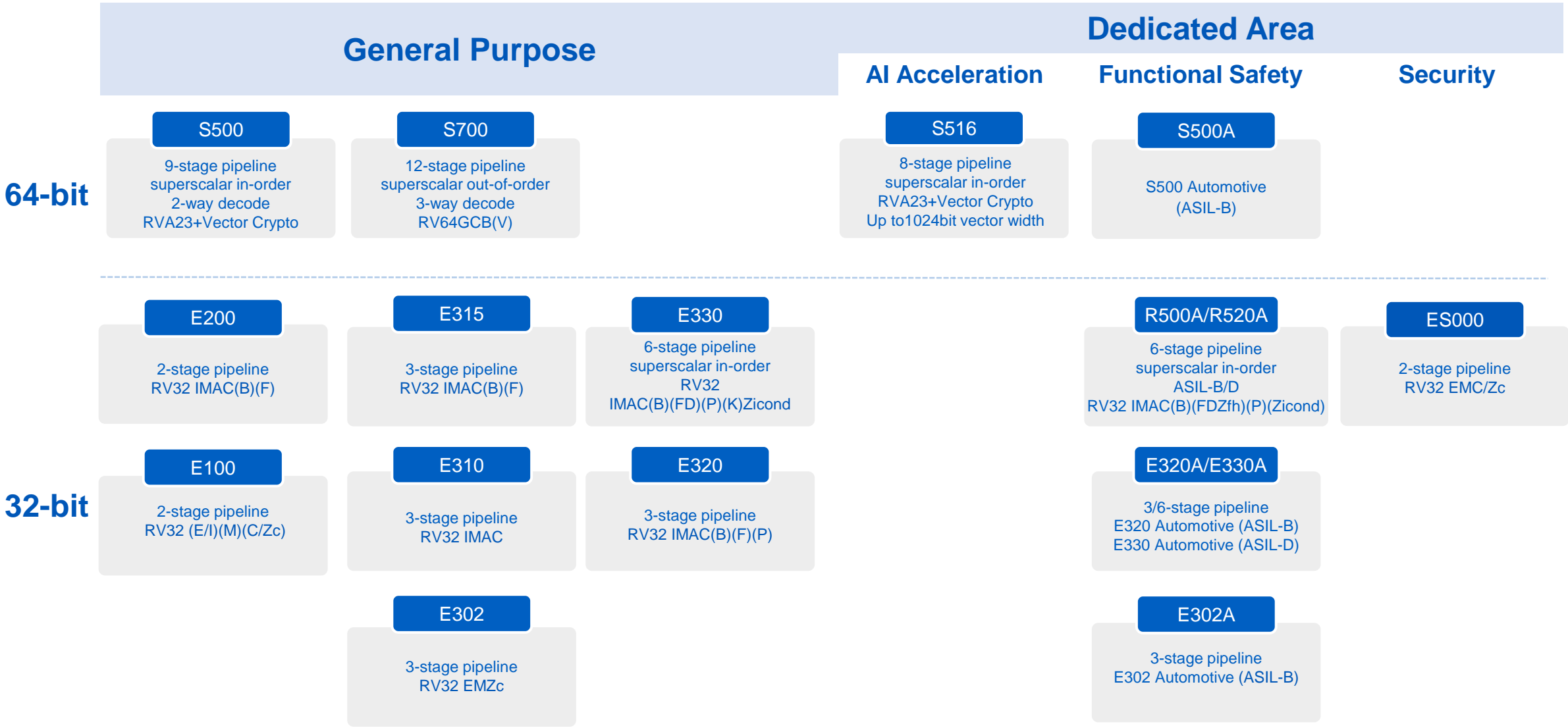
Rotating Chairman



Zhongguancun Standardization
Association(ZSA)

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II. ESWIN Computing's Product Innovation — RISC-V Cores



II. ESWIN Computing's Product Innovation — Key Products



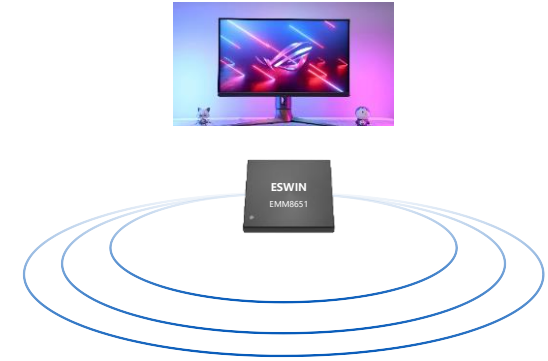
Micro LED Tiling Control Solution
ETS8682

- 32bit RISC-V CPU
- VBO/Mini-LVDS/USB/Ethernet interfaces
- Support Multi-scenario Micro LED Tiling Control
- Micro LED Real-Time Image Quality Calibration



Mini LED Multimedia Control Solution
EMS9012

- 32bit RISC-V CPU @200MHz
- 176KB SRAM, 512KB Flash
- 1000+ Zones Control
- Dynamic Brightness Adjustment
- LED Driver Mapping Control



Gaming Monitor System Solution
EMM86xx series

- 32bit RISC-V CPU
- HDR10, Local Dimming, 3DLUT
- Direct Ambient Light Control
- HW Enabled PIP/PBP
- HW Enabled Eagle Eye
- Simultaneous Free Sync & MPRT

II. ESWIN Computing's Product Innovation — Key Products



WiFi6/BLE Connectivity Solution
ECW6700

- 32bit RISC-V CPU @240MHz
- 2.4 and 5 GHz Wi-Fi 6 & BLE 5
- Low Power Consumption
- Support Matter and Multiple IOT Cloud Platforms



5G Small Base Station Transceiver Solution
ECT8676

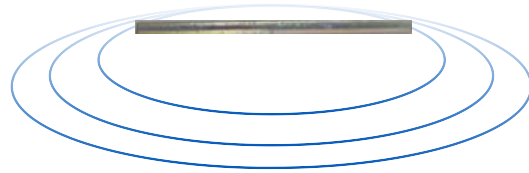
- 32bit RISC-V CPU @240MHz
- Frequency Range 1.7GHz-6GHz
- 2T2R1O SDR Structure, Bandwidth 200MHz
- Flexible and Configurable Frequency/Bandwidth



5G V2X System Solution
ECM9300

- 64bit RISC-V CPU @1.1GHz
- 2T2R Structure, Peak Rate 48.936 Mbps
- System Latency <20ms
- AEC-Q100 Grade2

II. ESWIN Computing's Product Innovation — Key Products



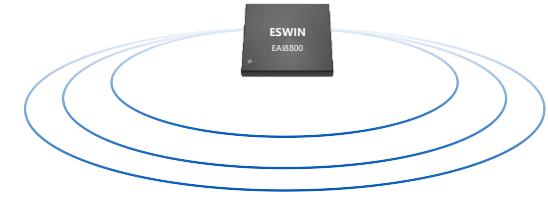
Automotive LCD Display and Touch Solution
EPD6501

- 32bit RISC-V CPU
- Support Max. 4K×1K Resolution
- 1920 Source Channels+960 Touch Channels
- Safety Function: OSD/Fail Detection/PCD etc.
- AEC-Q100 Grade2



Automotive OLED&LCD Touch Solution
EPH8630

- 32bit RISC-V CPU
- Support 8inch~18inch OLED&LCD Touch
- 240Hz Report Rate, SNR > 40dB
- 5mm Glove Touch
- AEC-Q100 Grade2



Automotive Camera Monitor System Solution
EA18800

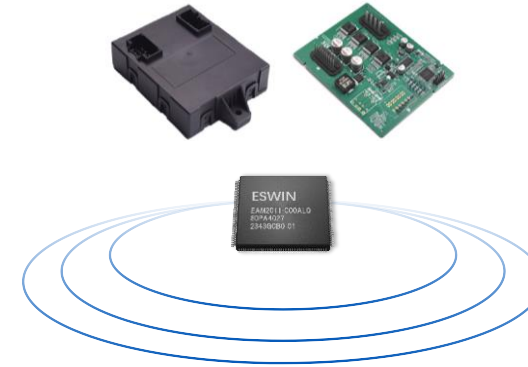
- 32bit RISC-V CPU
- Extremely Low Latency: End-to-End <20ms
- Startup <100ms, Sensor Configuration <500ms
- Provide complete set of solutions including motherboards, screen assemblies and cameras

II. ESWIN Computing's Product Innovation — Key Products



Automotive Motor Control Solution
EUA5103

- 32-bit RISC-V CPU
- 128KB Flash, 32KB SRAM
- Integrated LDO, LIN Transceiver, Motor Pre-Drive
- AEC-Q100 Grade1 Qualified



Automotive Control Solution with AI
EAM2011

- 32bit RISC-V CPU @160MHz
- AI-API/AI Framework and Algorithm Libraries
- RTOS/AutoSAR CP
- ESWIN IDE/AI Studio

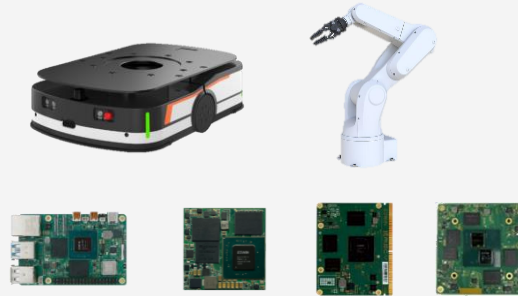
II. ESWIN Computing's Product Innovation — Key Products

AI PC



- **DeepComputing** DC-ROMA RISC-V AI PC
- Designed for Framework Laptop, with modular upgrades and open-source flexibility
- Local LLM support
- To be released TODAY

Machine Vision



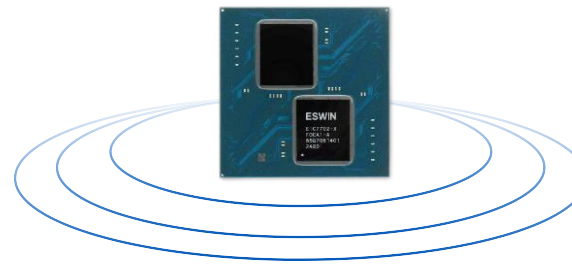
- More efficient in cost aggregation in stereo vision
- vSlam capability up to 1080P@240fps
- AI-based Point Cloud Completion algorithm
- Algorithms: 3DNR, HDR, AE, AWB, AF
-

AI Acceleration



- Memory Bandwidth up to 440GB
- Memory Capacity: 256GByte
- Computing Power: 160Tops
-

- 64bits RISC-V CPU (8 Cores/Out-of-Order Execution)
- Support Die-to-Die Cache Coherent Interconnection
- Self-developed NPU with 40TOPS @INT8

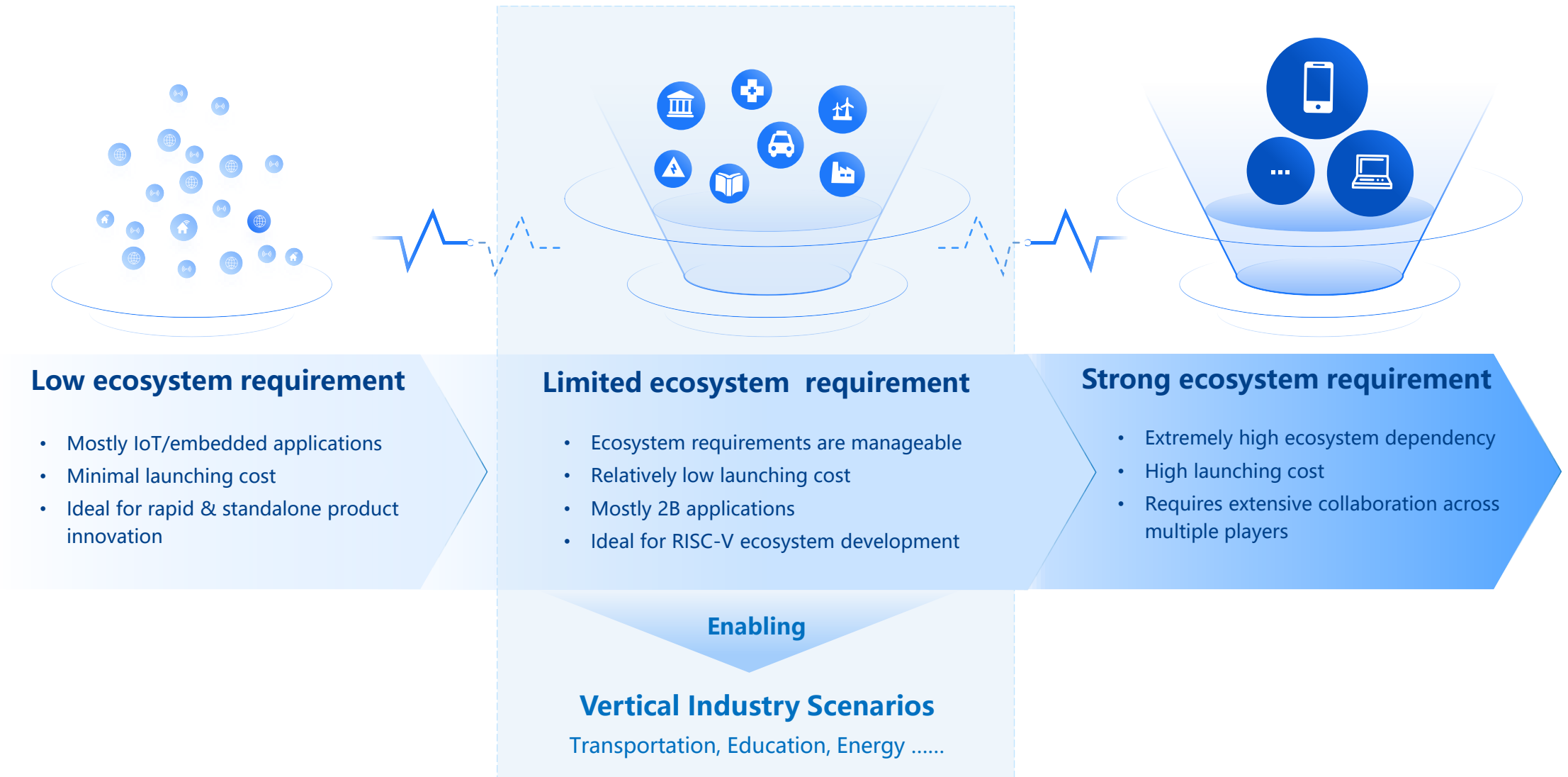


RISC-V Intelligent Computing SoC
EIC7702X

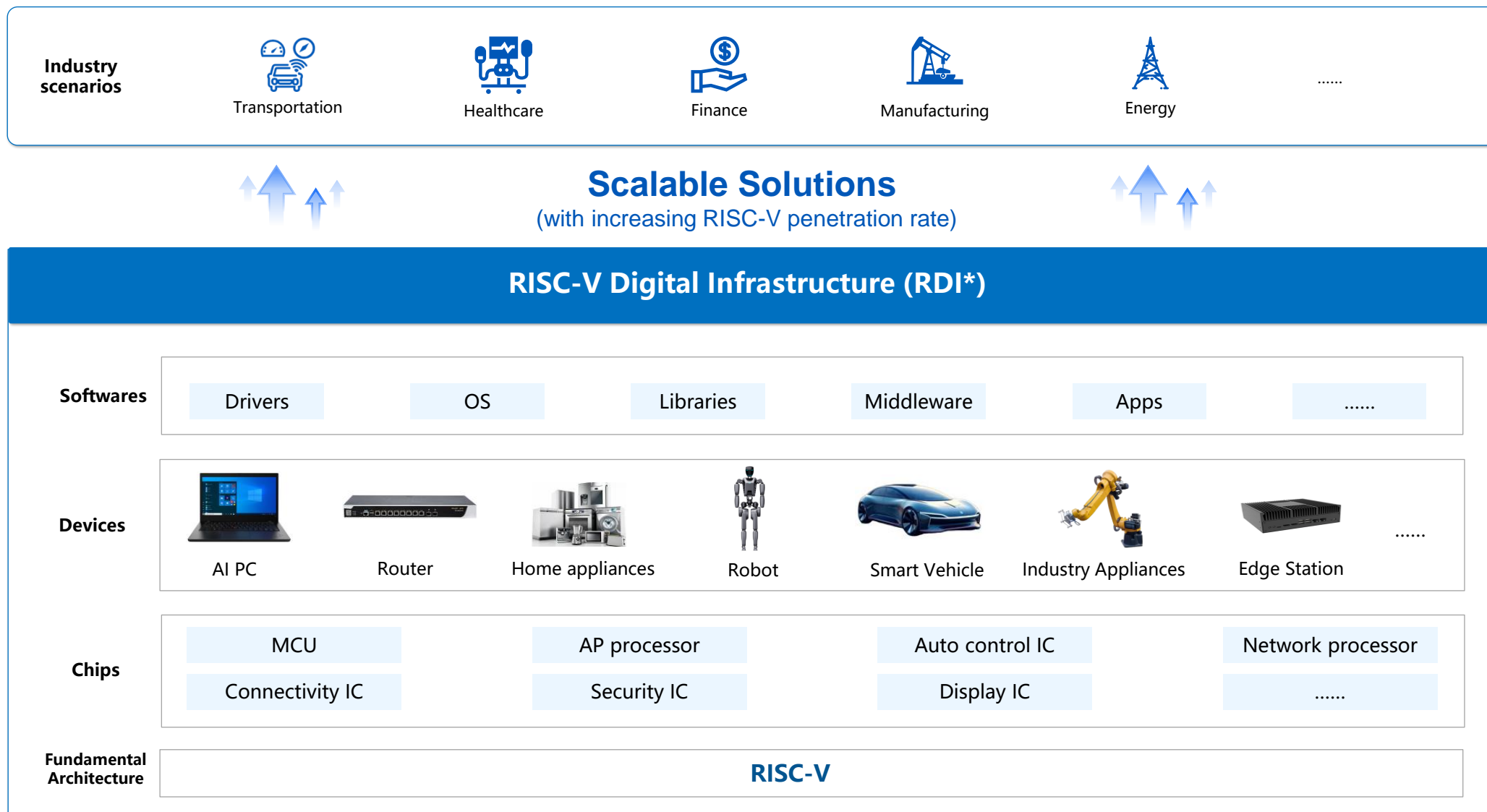
- Integrated video codecs that able to archives 8K@112fps on decoding and 8K@50fps on encoding
- Support high-precision Large Language Model (LLM)

- I. RISC-V: Challenges & Opportunities**
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- III. Enabling the Next Phase of RISC-V**

III. Enabling the Next Phase of RISC-V — Breakthrough for RV Ecosystem **ESWIN**

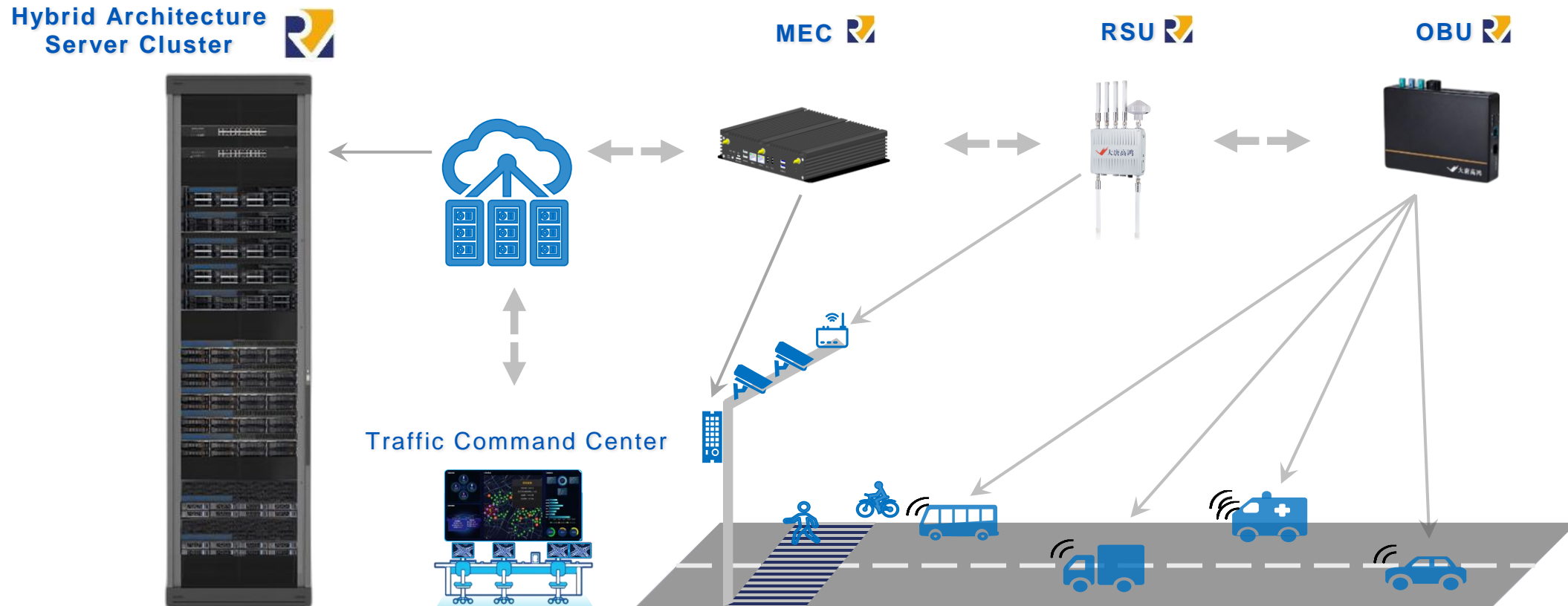


III. Enabling the Next Phase of RISC-V — RISC-V Digital Infrastructure



III. Enabling the Next Phase of RISC-V — Example of Industry Solutions

- **RISC-V V2X (Vehicle to Everything) system solution: The first systematic deployment case of RISC-V for the vertical industry scenario of intelligent transportation**
 - Build together with 10+ partners, some from the intelligent transportation industry.
 - The core devices already empowered by RISC-V, with more to follow.



A modern glass-walled office building with a grid-like facade, set against a clear sky.

Smart City

A blurred image of a stack of books and a container of colorful pencils.

Education

An aerial view of a multi-lane highway with a bridge crossing over it, surrounded by greenery.

Transportation

A close-up of several interlocking metallic gears.

Manufacturing

**Embrace RISC-V's New Opportunities,
Jointly Accelerate Industrial Deployment**

A close-up of a wooden pen resting on a spiral-bound notebook.

Smart Office

Two tall stacks of gold coins.

Finance

A pair of blue medical stethoscopes.

Healthcare

A three-bladed wind turbine against a blue sky.

Energy

Thanks

北京奕斯伟计算技术股份有限公司

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Official Account

