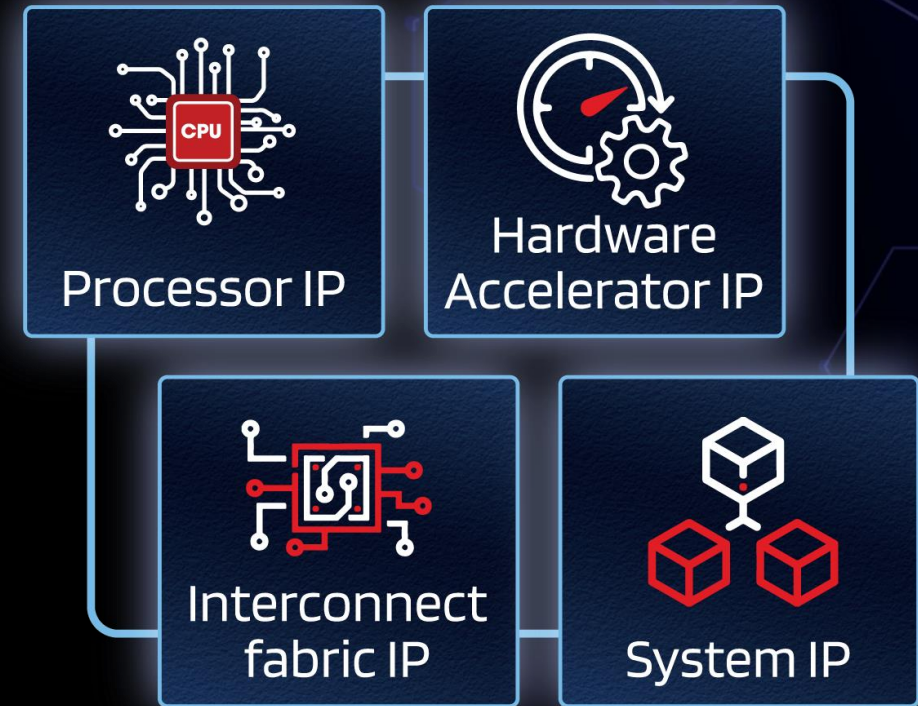RISC-V Summit 2025 : Paris, France

2 minute Lightening

Processor IP company (RISC-V)

Proven Performance Legacy

Team comprised of veterans from ThunderX2 ARM server chip development, Oracle, MIPS, Intel, Google

KLEINER PERKINS

Mayfield

Fidelity
INVESTMENTS

Processor IP

Hardware Accelerator IP

Interconnect fabric IP

System IP

Performance
CPU Compute

- Industry leading 25 SpecINT/GHz
- Simultaneous Multi-Threading support (2, 4 threads)

Performance
Data Compute

- Vector Extensions, up to 4096-bit
- Custom Instructions, Data types (example AI Non-Linear >10x)

Performance
Scalable Multi-Core

- Coherent Computer Cluster
- Akeana Mesh – 100's Coherent Core interconnect fabric

Performance
Data Movement

- Parallel Operation Heterogeneous core fabrics
- Data Movement engines

Thank you

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