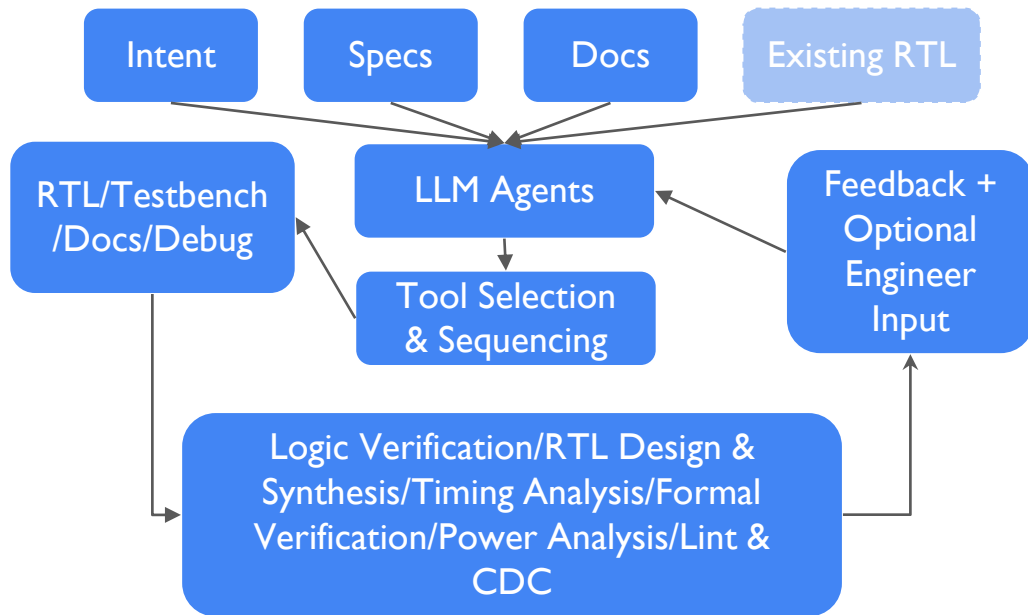


Revolutionizing RISC-V Chip Design with AI Agents

David Wang
Founding ML Engineer, ChipAgents

ChipAgents™: Agentic AI Powered Design & Verification Flow

- Input natural language specifications, documents, and instructions.
- Automatically generate RTL and test benches
- 97 % pass on VerilogEval-v2 benchmark



ChipAgents™: Agentic AI Powered Design & Verification Flow

