

Enabling Embedded Innovations





Enabling RISC-V Throughout the Whole SoC Life Cycle



Standardization

Significant contribution to debug & trace specifications

SoC Design

Expert support for SoC manufacturers implementing debug & trace infrastructure

Verification & Validation

Debug and test of SoC designs implemented in simulators and emulators

Bring-Up & SW Debug

SW debug, performance analysis and safety certification in all SoC scenarios

In-Field Testing

Testing of final products via XCP and USB interfaces



Your **KEY** to Embedded Innovations since 40 Years



THANK YOU!

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