

CVA6: Sovereign Open Source HW



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2024 key figures

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83,000 employees









Thales group Overview

Space

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Avionics

Defense

Communication

Cyber Security









Digital Sovereignty



Safe Secure





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Thales Innovation and Cooperation

New Spatial Onboard Computer:

- ☐ Reduce development cost and delay by leveraging open source assets
- Open source computing platform tailored and hardened for space application
- Experimental ASTRAL SoC: multi-CVA6 Core + OpenTitan (Secure Root of Trust) + Vector accelerator
- ■Mix-Criticality Software Platform











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Thales Innovation

Industrial Grade CVA6 µController released as Open Source

CV32A60X: Low area and High performance CVA6 Core

□100% Verified, Sovereign, ready for integration into silicon products (TRL5)!

□Focus on your Added Value IP / Differentiator, CVA6 comes as a commodity







Call to Action and Innovation!

Join forces between Research / Academia / Industry to mature Open HW IP from TRL 3 to TRL 5

Next step: CVA6 64b Application Core kicked off Q1/2025

- □Project CV64A60AX
- CEA targets TRL4 Verification target
- □Industry is welcome to verify up to TRL5







SPARE

Why Thales invests in RISC-V (and open-source HW)?

Share cost instead of purchasing IP

Open-source community

Sovereignty

Possible commercial exploitation without export constraints
Enable strong EU investment

Safety

No black-box

SWaP & customization

Exact fit between features and application needs

Security

A fully auditable processor

No vendor-locking

Business opportunities for support, customization...

Software

Large ecosystem compatible across implementations

Performance

State-of-the-art processor

Thales member of the OpenHW Foundation and RISC-V International



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