

Chips JU and the Vehicle of the Future – a RISC V view

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Outline

- 1. Chips JU and the European Chips Act.
- 2. The Automotive Action Plan.
- 3. Chips JU's vision for the Digital VoF and RISC V.
- 4. Chips JU Digital VoF projects' pipeline with budgets.
- 5. Highlights.





CHIPS JOINT UNDERTAKING AND THE EUROPEAN CHIPS ACT

Joint undertaking (JU)

A Joint Undertaking is an institutionalized **Public-Private Partnership** (**PPP**) with its **own legal identity**, with its own governance, budget etc.. The JUs are established by an EU regulation.

Chips JU

Chips JU was established in September, 2023, in an amendment to the Single Basic Act to implement the **first pillar of the Chips Act** and to continue the activities of its **predecessors** in the field of electronic components and systems (**ECS**). The Chips JU is a **tri-partite partnership** between the **EC**, the **participating states** and **European industries**; most of our actions are **funded jointly and equally** by these actors.

3IA - Chips JU private industrial members:









The European Chips Act

European Semiconductor Board (Governance)

Pillar 1

Chips for Europe Initiative

- Initiative on infrastructure building in synergy with the EU's research programmes
- Support to start-ups and SMEs

Pillar 2

Security of Supply

First-of-a-kind semiconductor production facilities

Pillar 3

Monitoring and Crisis Response

- Monitoring and alerting
- Crisis coordination mechanism with MS
- Strong Commission powers in times of crisis





AUTOMOTIVE ACTION PLAN (AAP) AND STRATEGIC DIALOGUE



Jan 30, 2025 – Kick-off
Mar 3, 2025 – Closing with President von der Leyen

Feb 17, Strategic Dialogue with 5 EVPs/Commissioners

26 participants, 4 Focus topics:



- Software-Defined Vehicles (SDVs) for connected, autonomous, and electric mobility
- 2 Automotive Hardware and critical technologies
- 3 Data Access and Pooling
- 4 Regulatory simplification

Hand-over of the "**Declaration to collaborate on an EU Software-Defined Vehicle**" signed by 15 OEMs, Tier 1 suppliers, and DE, FR, IT Automotive associations



March 5, 2025 -

Publication of the Action

Plan for Automotive

The outcome of this dialogue was a comprehensive **Action Plan with 5 Chapters**:

Innovation and Digitalisation

Decarbonisation

Competitiveness and supply resilience

Skills and Social dimension

Level playing field and business environ.



Chipsjij will play a key role in implementing the AAP.





CHIPS JU'S VISION:

THE DIGITAL VEHICLE OF THE FUTURE AS A HOLISTIC TECHNOLOGY STACK

Coordination a Support

Software-Defined Vehicle (SDV) platform

Call 2024-1-IA T3 - Shift2SDV (Negotiations)

SDV Applications & Services,

Drivers, Middleware, SDV OS (RTOS), etc.

Hardware Abstraction Layer

Call 2023-2-RIA T2 - HAL4SDV (Granted)

Automotive Hardware RISC-V° platform

2024-1-IA T2 — **Rigoletto**

NEW IN CALLS 2025 *

Microprocessors (RISC V), Microcontrollers, HW accelerators, sensors, interconnect, SoC, etc.

Automotive HW topics:

2025-IA FT 1 RISC V Automotive Hardware Platform; Call 2025-IA HIA Heterogeneous integration for high-

performance automotive computing.

Automotive System integration topic:

2025-IA FT 2 Al-assisted Methods and Tools for Software-Defined Vehicle Engineering Automation.

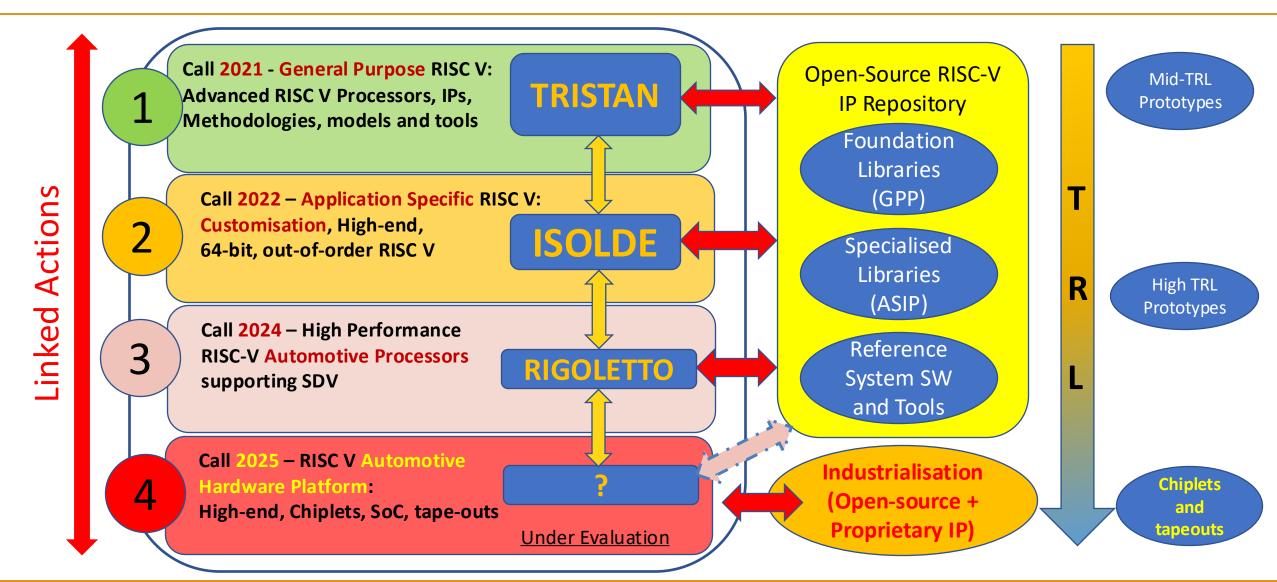




2025

NEW IN CALLS

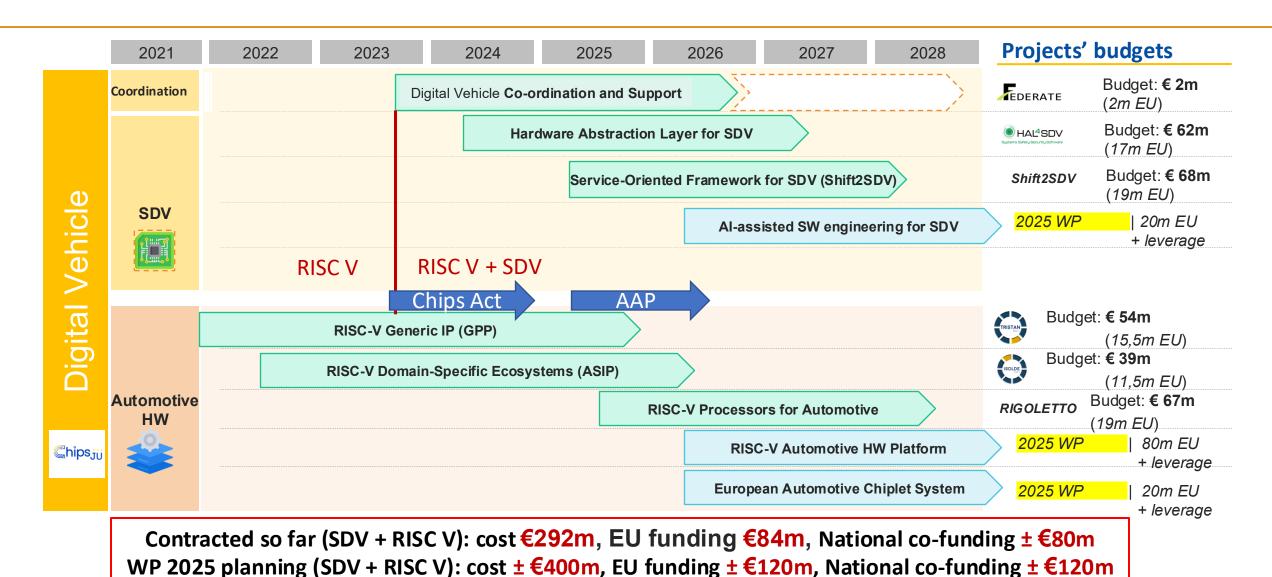
RISC V Focus Topics Since 2021: Chips JU Top-Down Approach







DVoF and RISC V: Chips JU Projects Pipeline







HIGHLIGHTS

- Chips JU plays a unique key role in the implementation of two important EU Initiatives the European Chips Act and the Automotive Action Plan.
- Chips JU's tri-partite model leverages EU public and private resources for higher impact on the European Industry in general, automotive being of specific strategic priority.
- Chips JU's ambition is to be a key driver in promoting and establishing RISC V as the architectural base of a standard hardware platform tailored for the automotive industry.
- Chips JU's top-down approach consolidates efforts on SDV and RISC V based design across the entire
 technology stack of the Digital VoF system integration, computer architecture, microarchitecture,
 chiplets, tape-outs but also software, middleware, and tools.
- Bottom-up Chips JU activities complement the presented top-down approach numerous other projects addressing the ECS Strategic Research and Innovation Agenda (ECS SRIA) employ or develop RISC V.
- Chips JU's mission is to support EU industry's competitiveness and technological sovereignty by keeping Europe at the Forefront of Technological Development the Automotive case has been presented.



Thank You!



