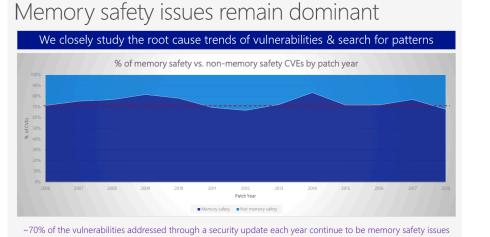
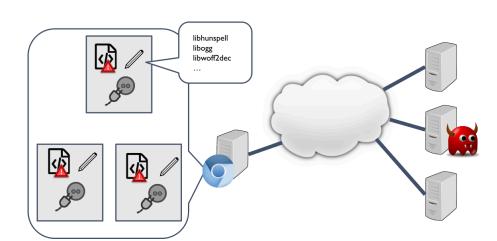
CAPSTONE: An Architecture Design for Expressive Security

Jason Zhijingcheng Yu[†], Prateek Saxena School of Computing, National University of Singapore † final-year PhD student open for hiring

Motivation: Patchwork of Security Extensions

Security Challenges





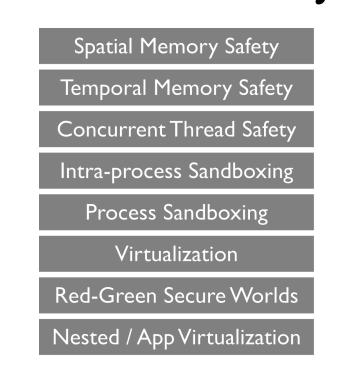
4 Ubuntu Lini Linux Kernel 9 Iphone Os

Memory Safety

Fine-grained Isolation

Confidential Computing

Patchwork of Security Extensions



[Intel MPK, x86/64 DEP/NX] [Intel MPX, RISC-V/ARM CHERI] [ARM MTE] [Intel <u>TSX</u>] [ARM TME] [Intel SGX] [Intel MPK] [x86/64 Privilege Rings] [AMD SEV] [Intel VT-x] [Intel TDX] [ARM CCA] [ARM <u>TZ</u>] [Intel <u>TXT</u>] [Intel VT-x] [Intel SGX]

Problem: Compose Security Extensions?

Example: SGX

+ Exception handling (Cui et al., 2021)

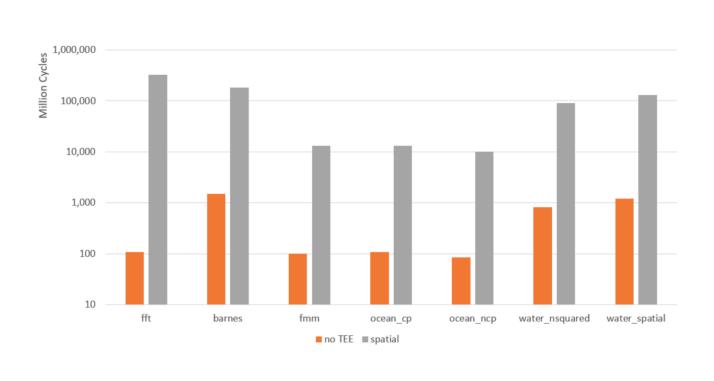


Arbitrary code execution

Affecting 9 SGX runtimes

CVE-2021-0186, CVE-2021-33767

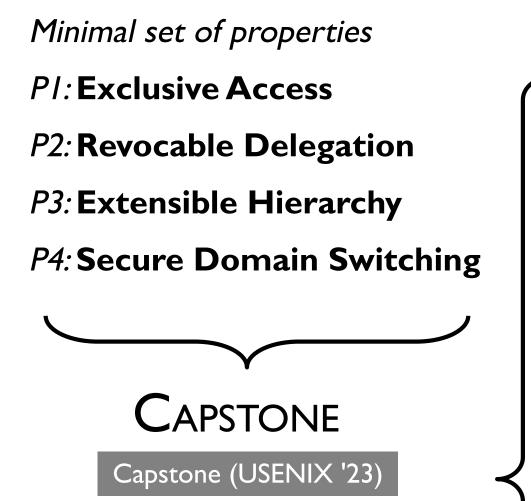
+ Memory sharing (Yu et al., 2022)

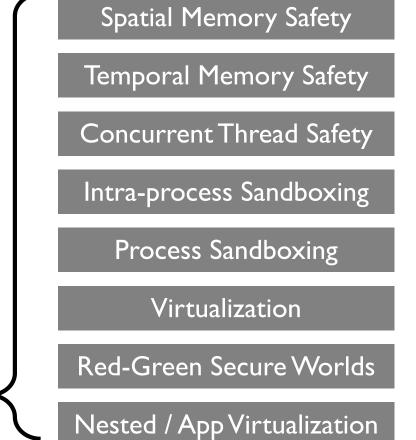


2–3 orders of magnitude overhead

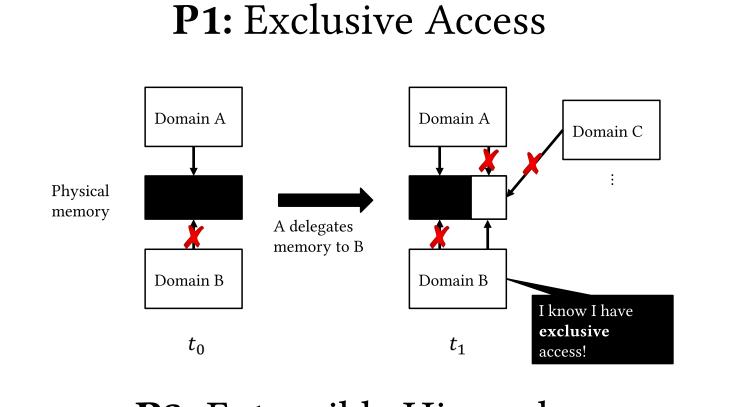
Goal

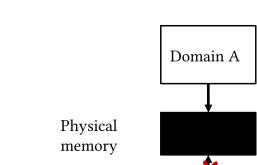
Can one design a unified foundation for multiple security goals? (Yu et al., 2023)



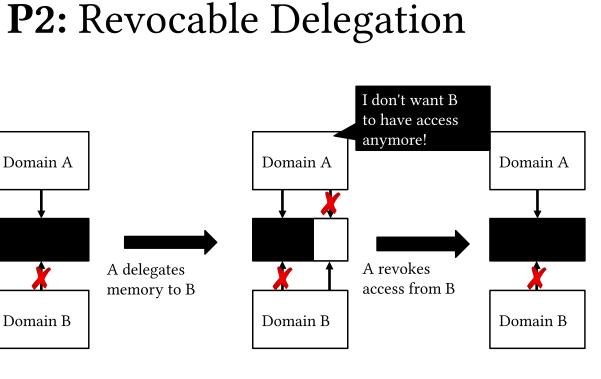


Desired Properties

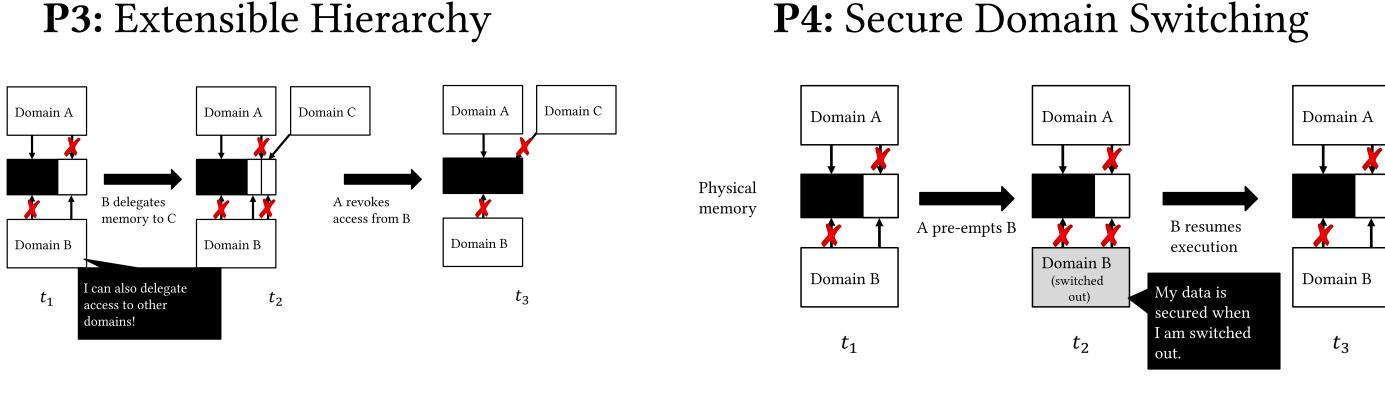




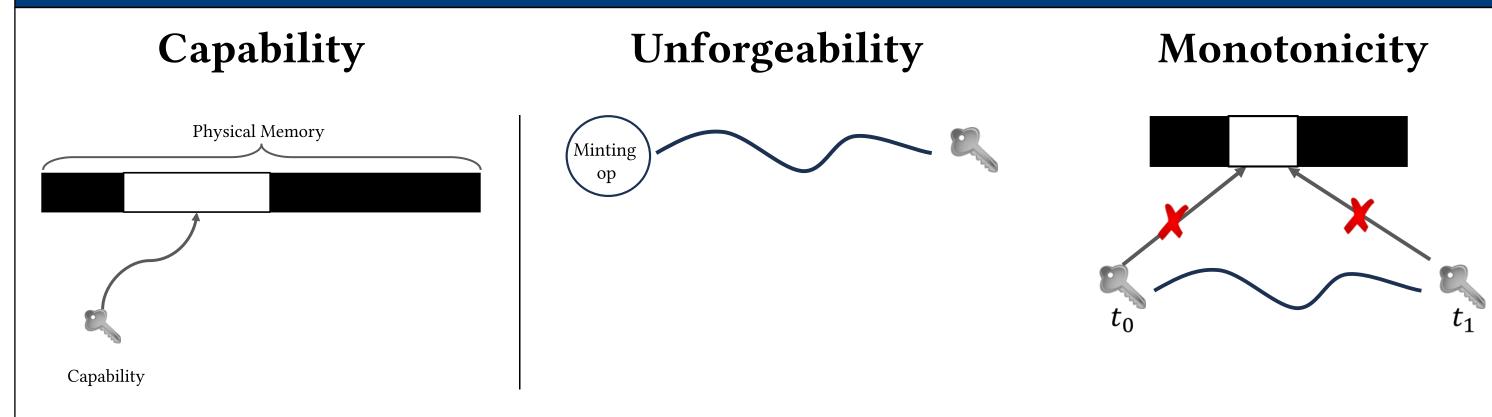
Domain B



P4: Secure Domain Switching

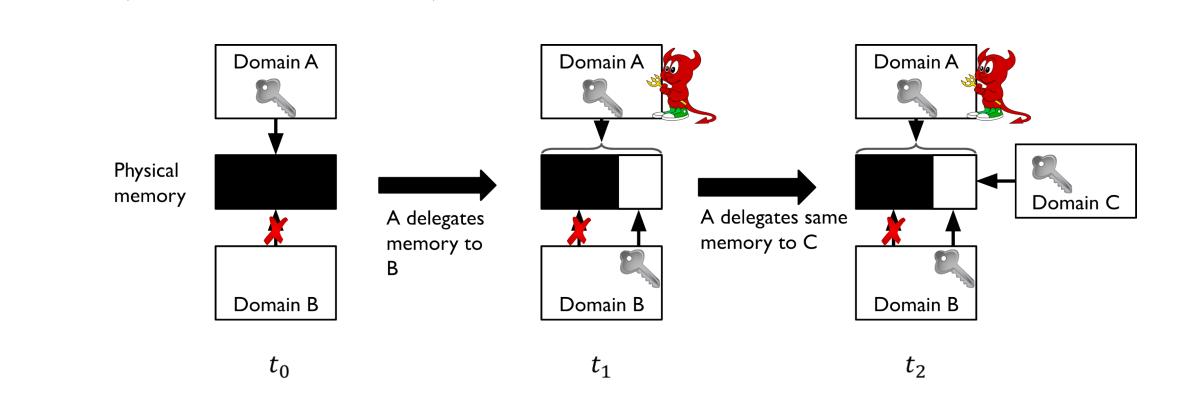


Base Design: Capability-based Security



Base Capability-based Model Is Insufficient

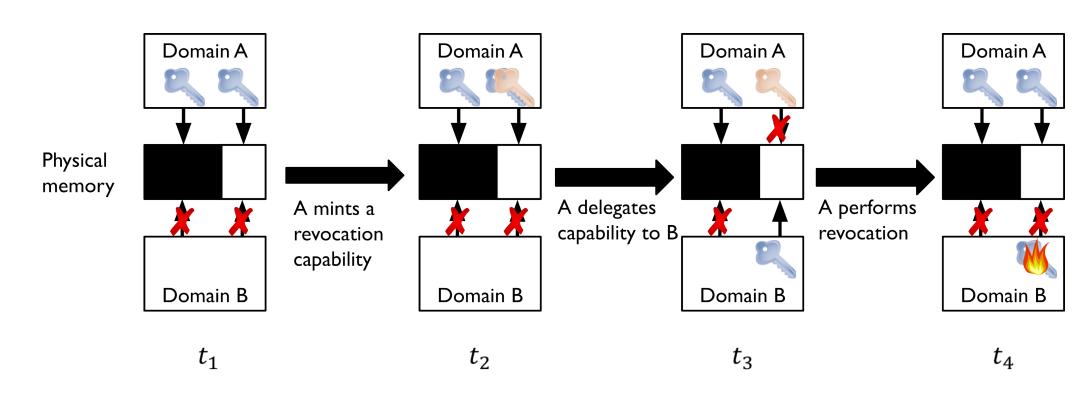
Example: P1 (Exclusive Access) cannot be achieved



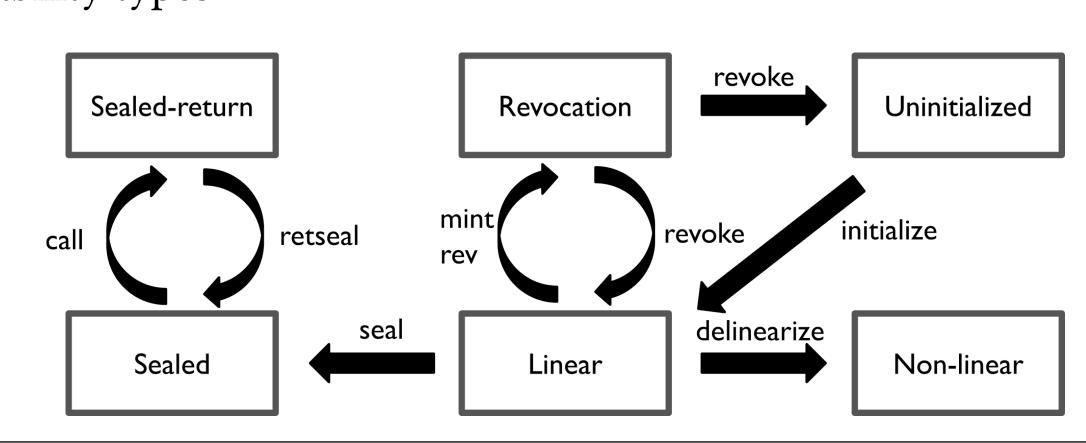
Capability-based Model in Capstone

- Linear capability: Non-duplicable
- Revocation capability: A capability "snapshot", usable only for revocation

Example:



Other capability types:



Example Use Cases

