

Tenstorrent IP

Control your Core. Own your Future.

Troy Jones



RISC-V Turned a Hardware Need Into an IP Business

The Need

A hardware gap had to be solved

- Needed leading AI + CPU for performance
- Market options did not fully fit
- A requirement to optimize for our own systems



The Choice

Open-specification gave us the right path

- More control and flexibility
- Avoided proprietary lock-in
- Better fit for costs and customization



The Result

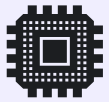
That capability became a business

- Proven first in Tenstorrent hardware
- Customer demand emerged externally
- Created a licensable IP business

RISC-V enabled Tenstorrent to build differentiated technology, validate it in silicon, and turn it into a scalable IP business

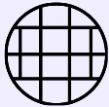
Control Your Silicon Future

Open by design. Yours by choice.



IP

Industry-leading AI, CPU and Chiplet IP for performance and efficiency



Silicon

High-performance chips built for scalable AI



Systems

Reference and production systems at scale



Software

Open-source software optimized for Tenstorrent silicon



Cloud

Cloud-native platforms for deployment and scale



Full-stack AI company

Software, IP, Silicon, Systems, and Cloud built together



Open and transparent

Open-source software, active developer community, and straightforward pricing

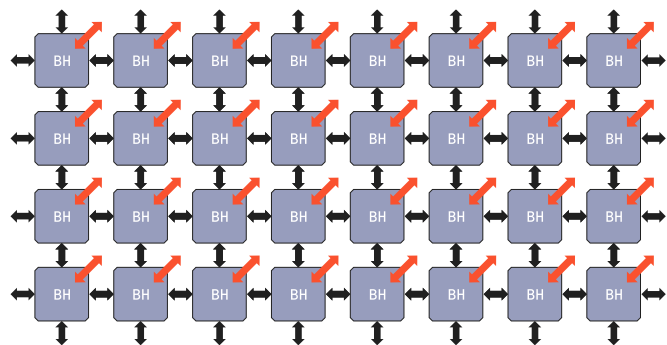


Customer control

Build, customize, deploy, and license without vendor lock-in

Tenstorrent RISC-V at Massive Scale

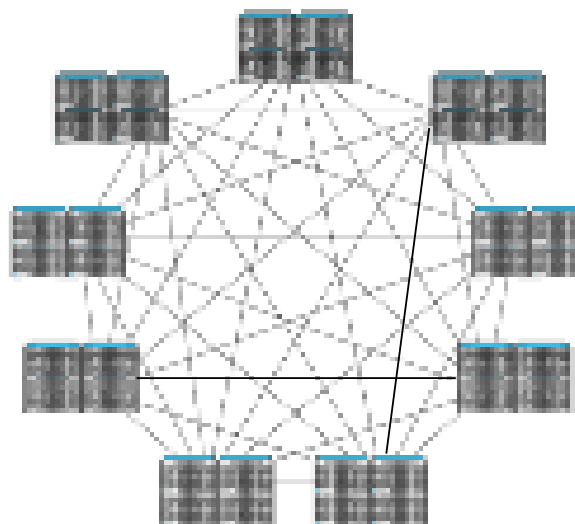
Galaxy Blackhole



24,576

RISC-V cores

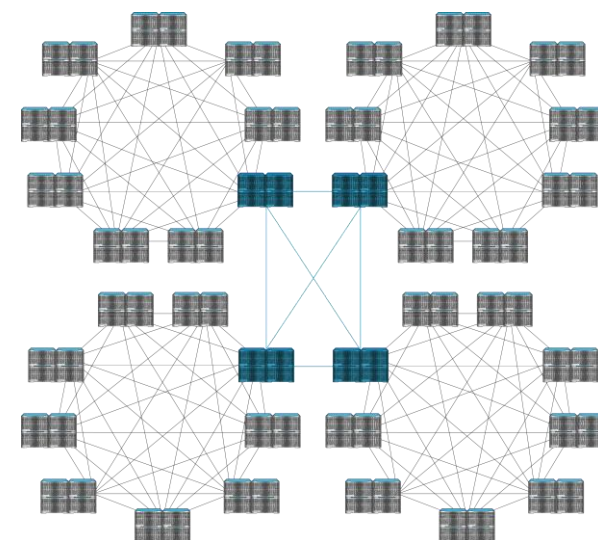
Galaxy Supercluster 36



769,536

RISC-V cores

Galaxy Supercluster 144



~3.0M

RISC-V cores

Control Your CPU Roadmap

01

Control Your Roadmap

- ✓ Set your own direction, timelines, and differentiation
- ✓ Reduce dependence on someone else's priorities and release cycle

02

Build on Your Terms

- ✓ Customize for performance, features, and system fit
- ✓ Align the CPU to your workload and platform strategy

03

Increase Strategic Flexibility

- ✓ Adapt faster as requirements evolve
- ✓ Create more room for product and market differentiation

04

Reduce Long-Term Dependency

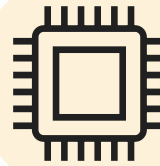
- ✓ Move away from rigid vendor-defined constraints
- ✓ Build lasting leverage across future programs

Why Tenstorrent RISC-V IP?



Open business model

Ability to modify and extend the IP



Proven silicon and systems

CPUs, AI Accelerators, System IP, Chiplets



Built by strong teams

Our architects bring experience across leading semiconductor, hyperscale, automotive, and consumer technology companies



Fully Verified

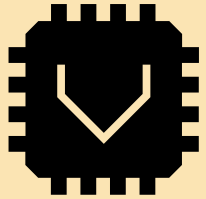
Simulation, emulation, FPGA, compliance

Control your Core. Own your **Future.**

Tenstorrent IP: Designed to Scale

2025 – Shipping today

Ascalon X



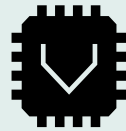
Application CPU

Tensix NEO

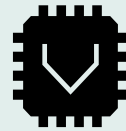


AI Acceleration

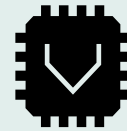
2026 – CPU & AI IP Products



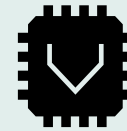
Ascalon X
Application CPU



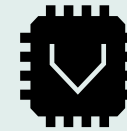
Ascalon S
Application CPU



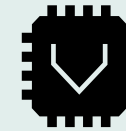
Alexandria
FuSa App CPU



Babylon
Application CPU



Dirk
Realtime CPU

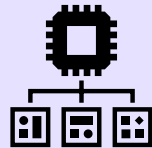


Rondel
Embedded CPU

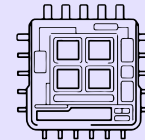


Tensix NEO/
Trinity
FuSa AI IP

2026 – Chiplets & Subsystems



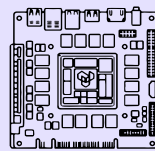
Aegis
Ascalon Chiplet



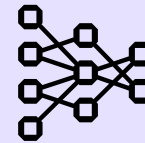
Ascalon Arsenal
Compute Subsystem



Quasar
Tensix Chiplet



Atlantis Platform
Ascalon Dev Sys

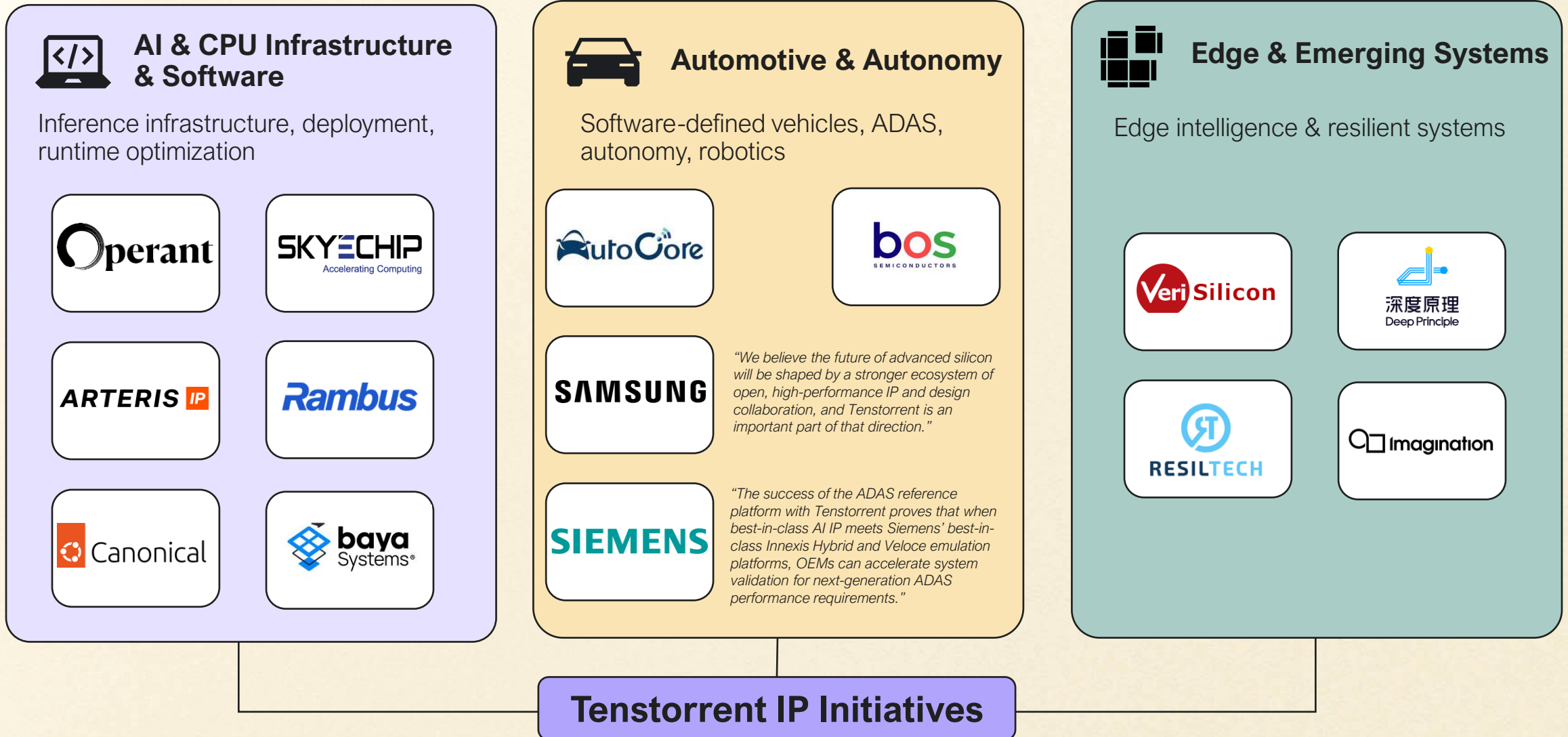


AI Stack
Models Infrastructure



CPU SDK
RISC-V SW Dev. Kit

Enabling new markets through ecosystem collaboration



What Others Are Saying



“Tenstorrent now sits firmly in the open, multi-vendor AI silicon category with the low volume production credibility, software breadth, customer roster, and benchmark transparency required to be evaluated in serious enterprise and sovereign procurement processes.”

Patrick Moorehead
CEO, Moor Insights & Strategy

“With its combination of RISC-V CPU IP, AI acceleration IP, and internally developed system interconnect IP, Tenstorrent offers one of the most highly valued IP portfolios in the market for companies looking to own and differentiate their own silicon and systems.”

Richard Wawrzyniak
Principal Analyst, The SHD Group

“Tenstorrent's focus on generality, supporting 2.5 million open-source models and compiling from multiple frameworks, sets it apart from closed approaches. The company now represents a bet on the future of RISC-V processors to power a globally open innovation ecosystem built on open-source and sovereign AI models.”

Brendan Burke
Senior Analyst, The Futurum Group

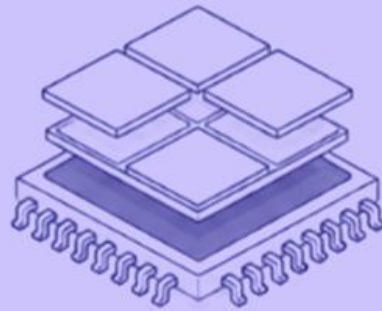
Innovation License: Control Your Roadmap

Traditional Licensing



- Use what the vendor ships
- Limited flexibility
- Vendor-defined roadmap

Tenstorrent's Innovation License



- Modify the licensed IP
- Access RTL and key collateral
- Build on open spec. RISC-V
- Flexible business model

Customer Results



- Differentiate your silicon
- Optimize for your workload
- Reduce/remove vendor lock-in
- Control your roadmap



Thank You

